CS 6530: Advanced Database Systems Fall 2023

Lecture 02 Data system architecture

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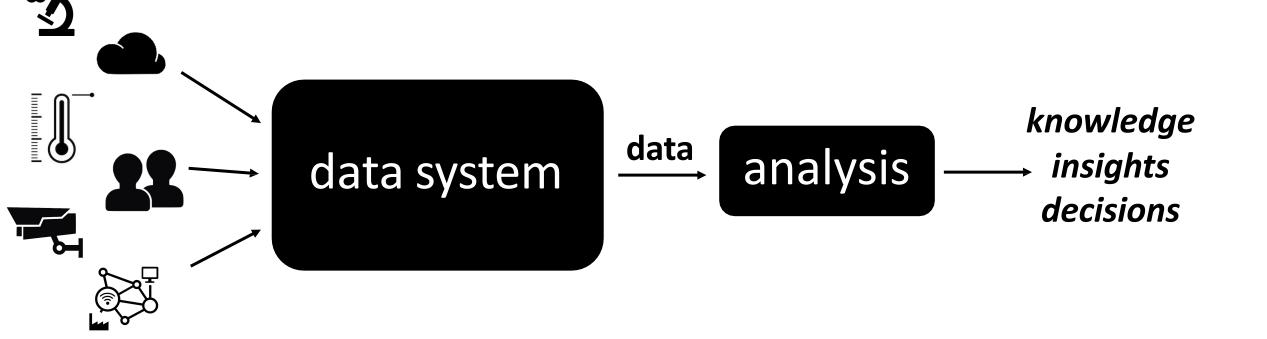


Data system architecture essentials

Acknowledgement: Slides taken from Prof. Manos Athanassoulis, BU



A data system is a large software system that stores data, and provides the interface to update and access them efficiently





Growing need for tailored systems



new applications





new hardware





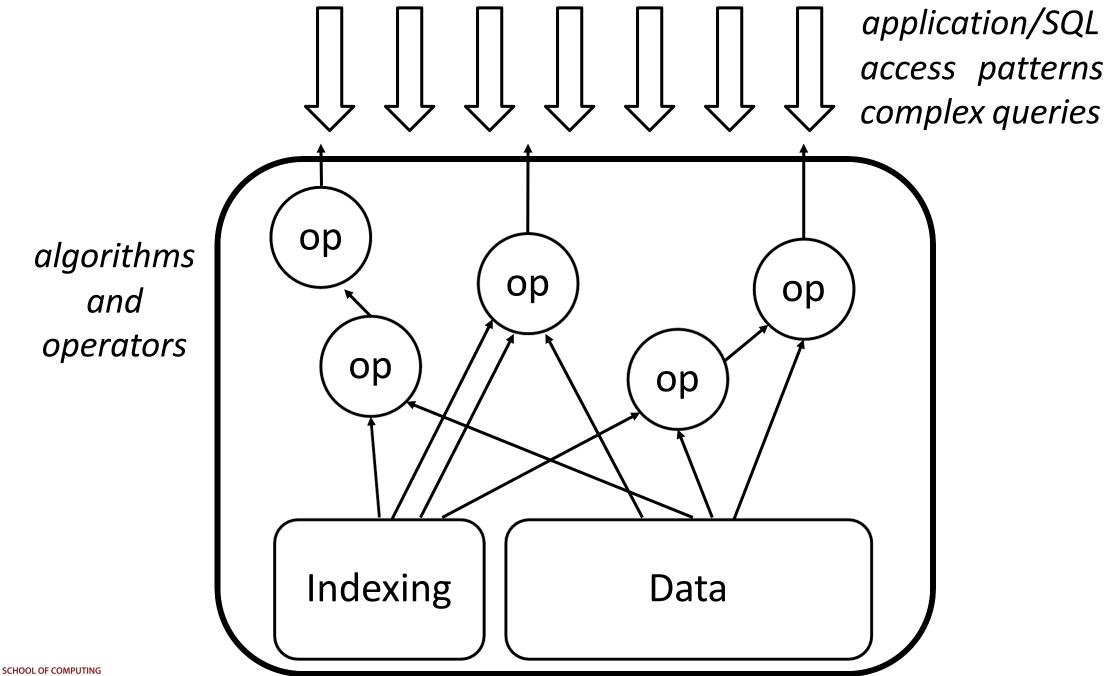
more data



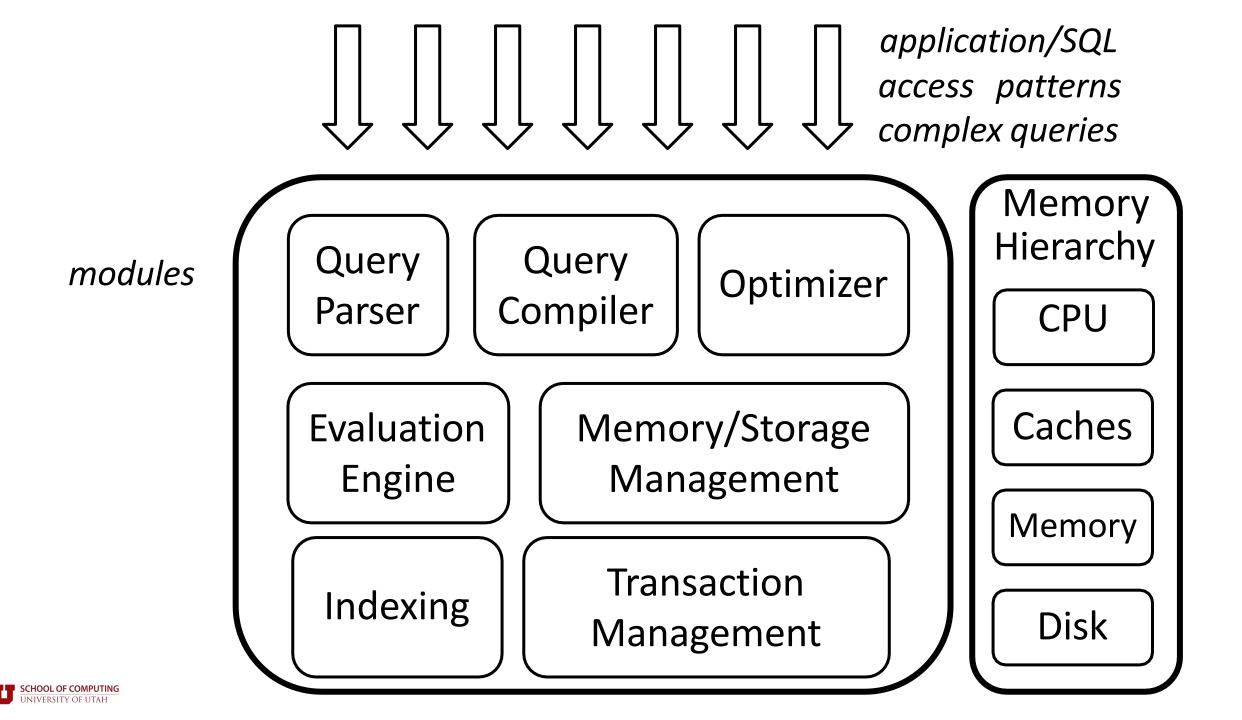
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Data system, what's inside?





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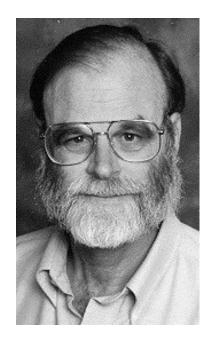
Data system, what's underneath?



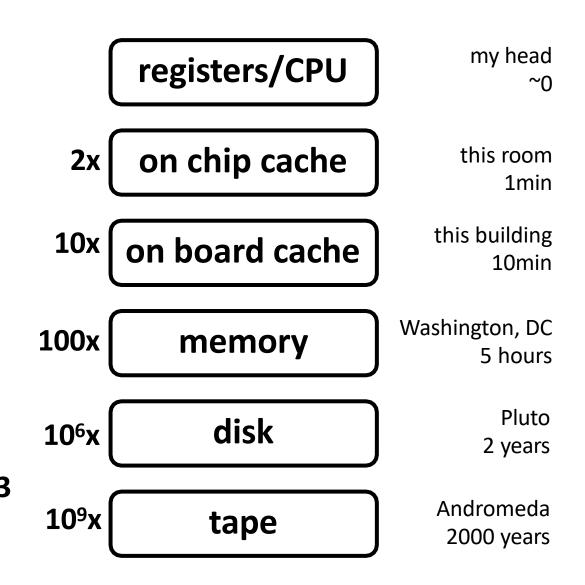
Memory hierarchy CPU (intel) Core™i7 on-chip cache on-board cache smaller faster main memory more expensive (GB/\$) flash storage disks flash

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Memory hierarchy (by Jim Gray)



Jim Gray, IBM, Tandem, Microsoft, DEC "The Fourth Paradigm" is based on his vision ACM Turing Award 1998 ACM SIGMOD Edgar F. Codd Innovations award 1993



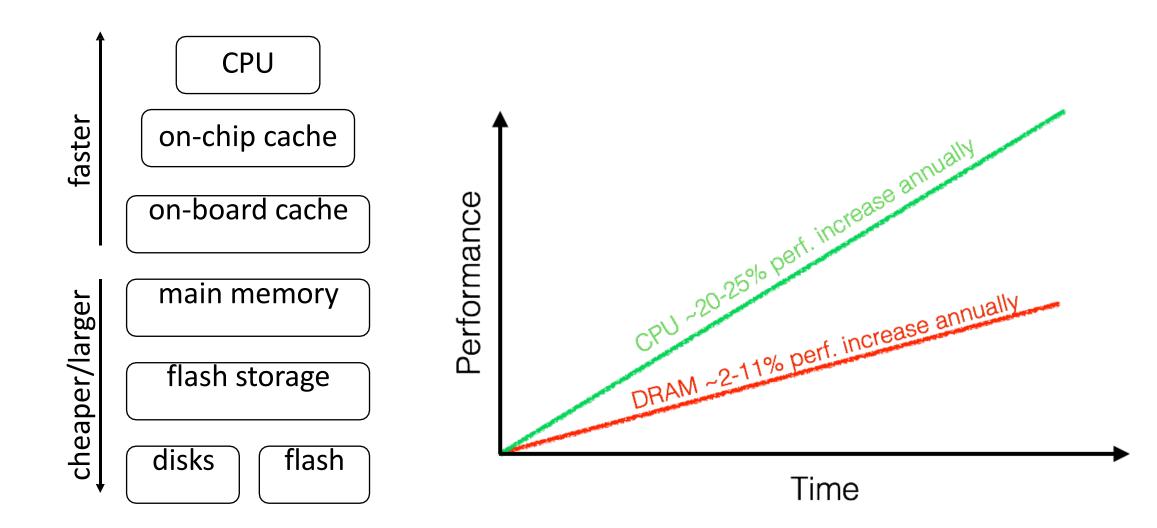


Memory hierarchy and latencies

Memory	Size	Latency	Bandwidth
L1 cache	32 KB	1 nano sec	1 TB/sec
L2 cache	256 KB	4 nano sec	1 TB/sec (shared by cores)
L3 cache	8 MB or more	~30-40 nano sec	> 400 GB/sec
Main memory DDR DIMM	4 GB to 1 TB	~80-100 nano sec	100 GB/sec
I/O devices on memory bus	6 ТВ	100X-1000X slower than memory	25 GB/sec
I/O devices on PCIe bus	Limited only by cost	Milli sec – minutes	GB-TB/hour (depends on HW and distance)

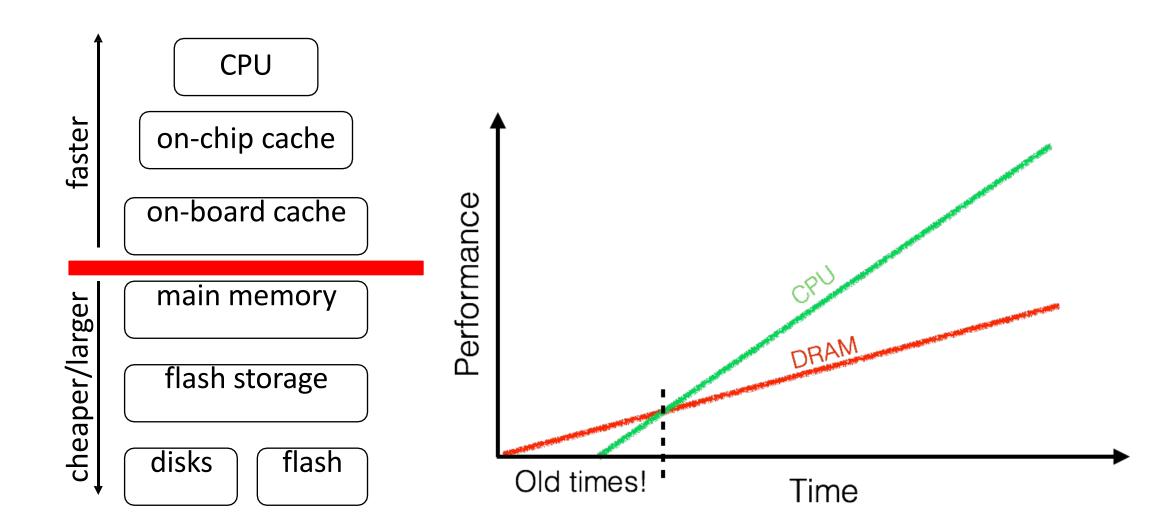


Memory wall



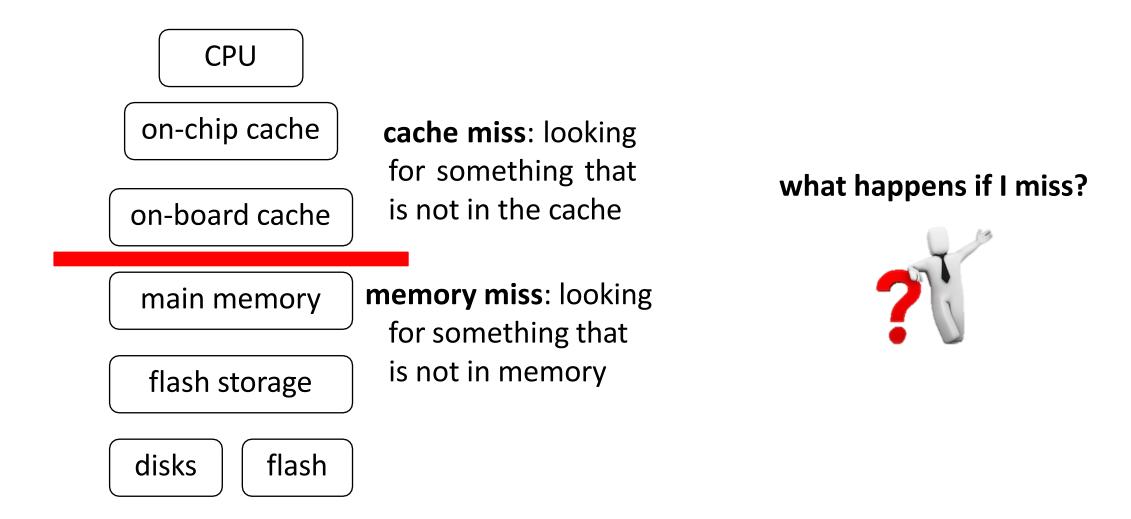


Memory wall

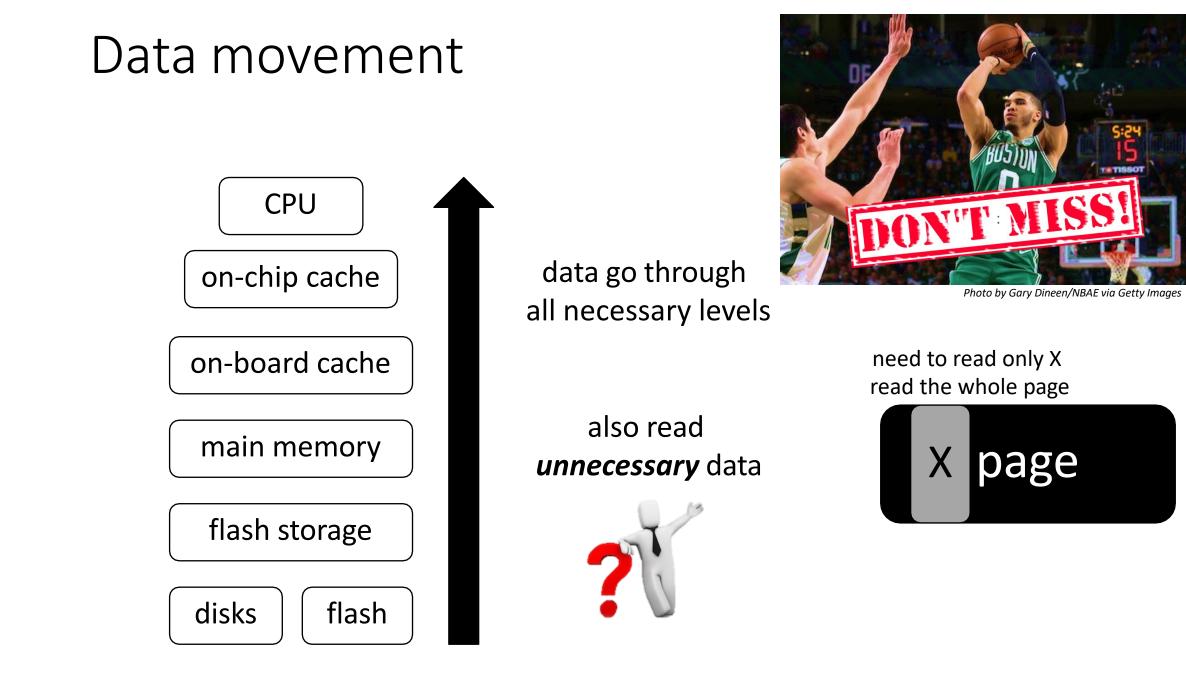




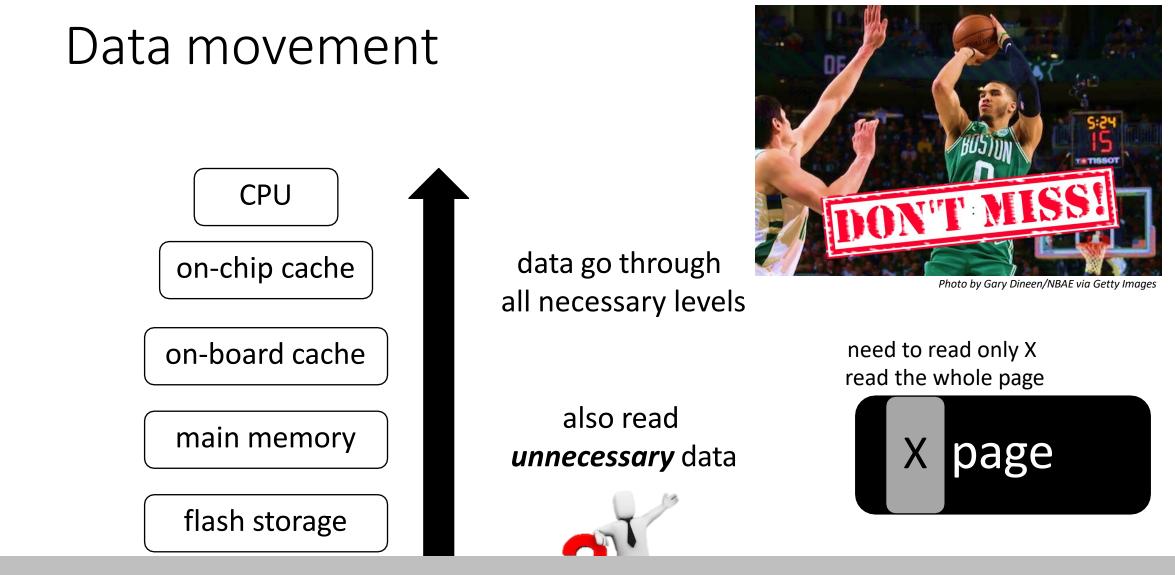
Cache/memory misses



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remember!

disk is millions (mem, hundreds) times slower than CPU

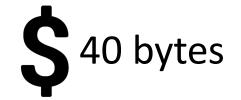


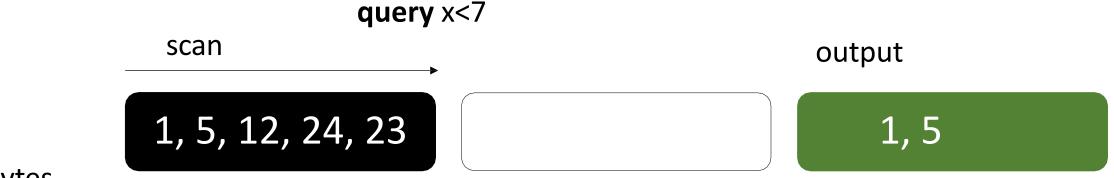
scan



disk (memory level N+1)





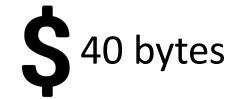


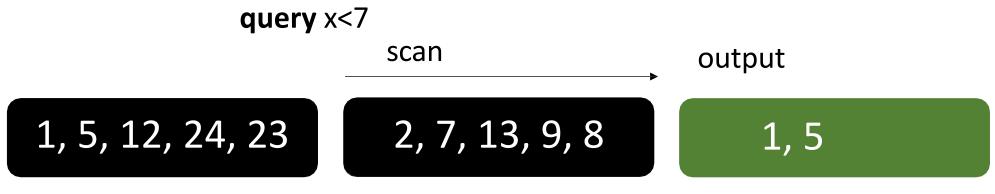
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memory (memory level N)

disk (memory level N+1)





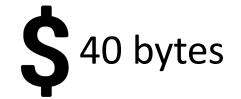


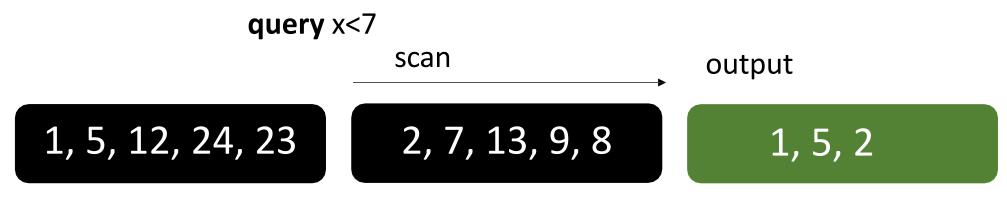
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memory (memory level N)

disk (memory level N+1)





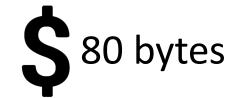


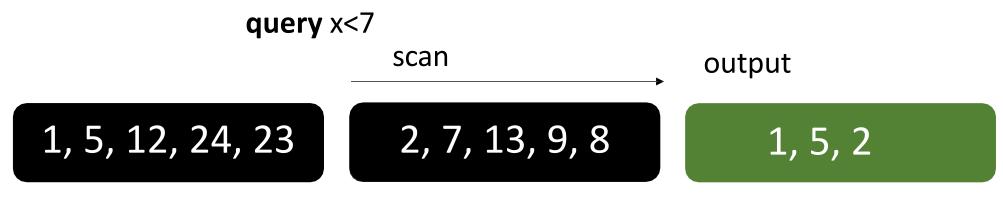
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memory (memory level N)

disk (memory level N+1)





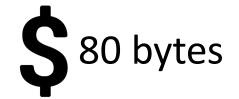


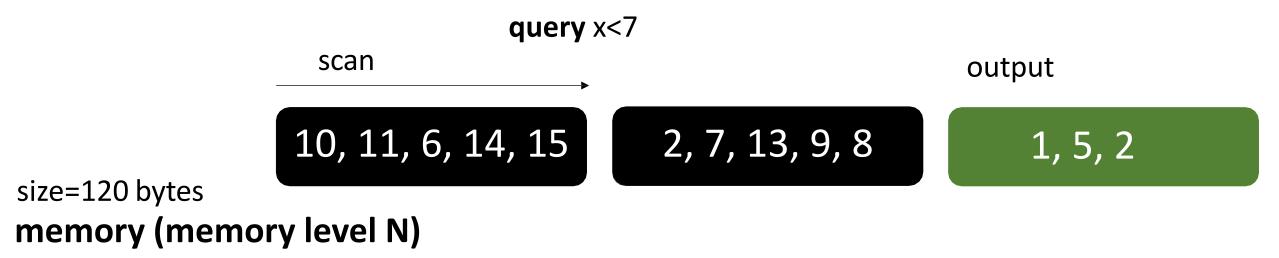
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memory (memory level N)

disk (memory level N+1)



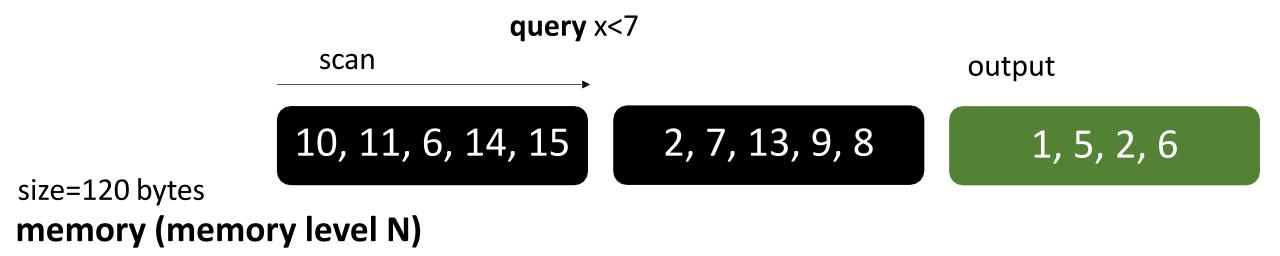




disk (memory level N+1)

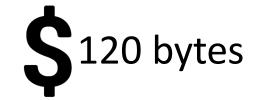


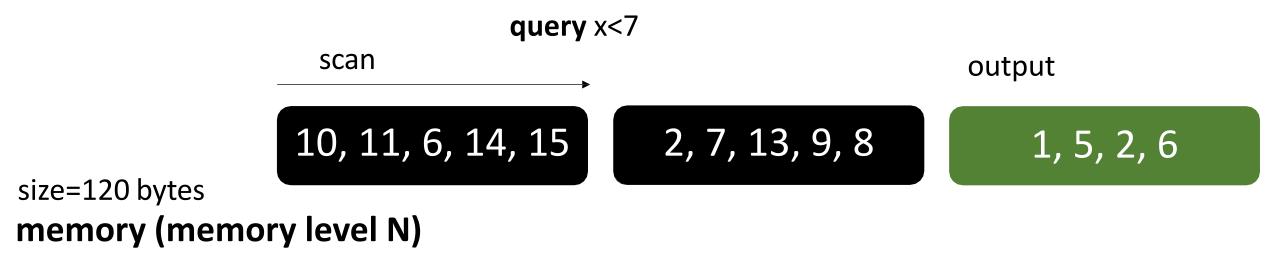




disk (memory level N+1)







disk (memory level N+1)



What if we had an oracle (perfect index)?





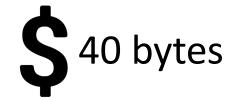


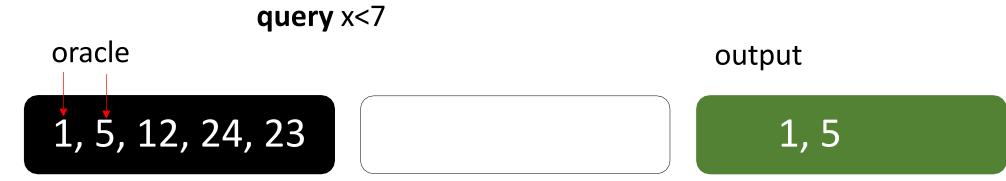
scan



disk (memory level N+1)





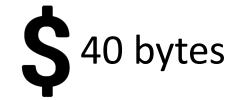


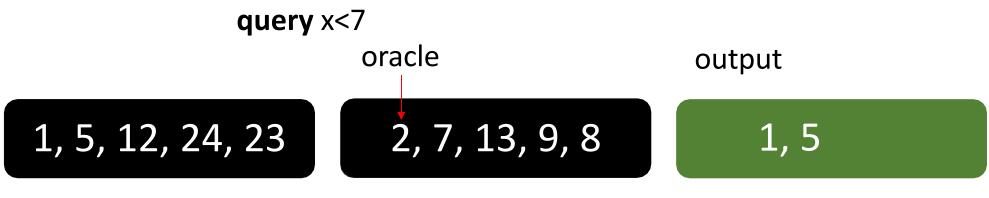
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memory (memory level N)

disk (memory level N+1)



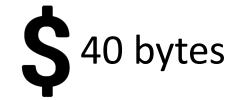


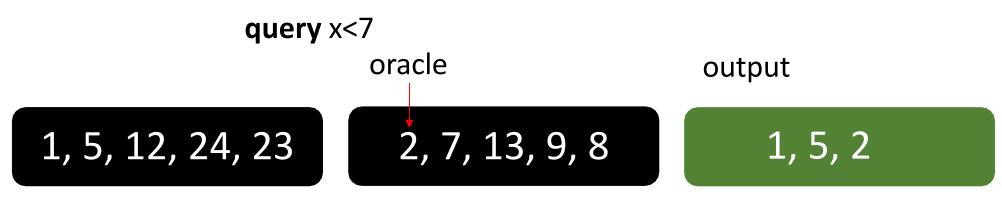


size=120 bytes memory (memory level N)

disk (memory level N+1)





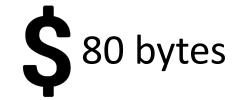


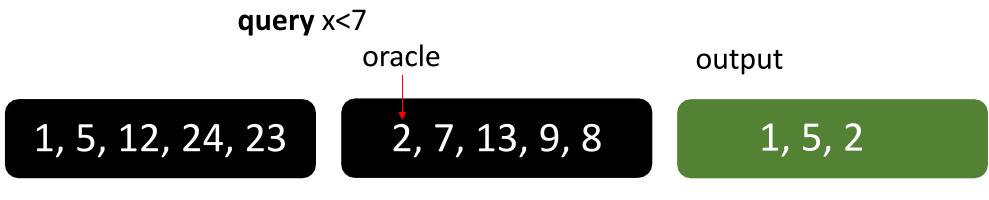
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memory (memory level N)

disk (memory level N+1)





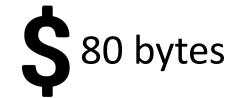


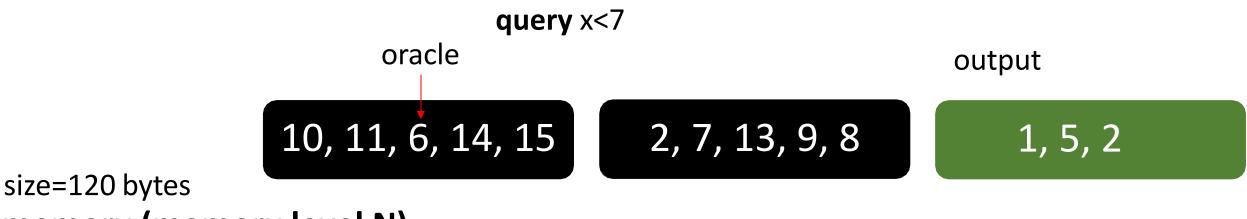
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memory (memory level N)

disk (memory level N+1)



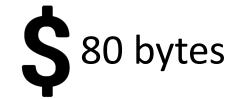


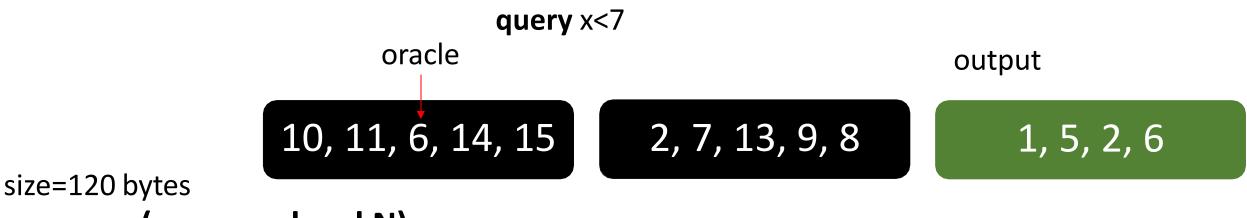


memory (memory level N)

disk (memory level N+1)



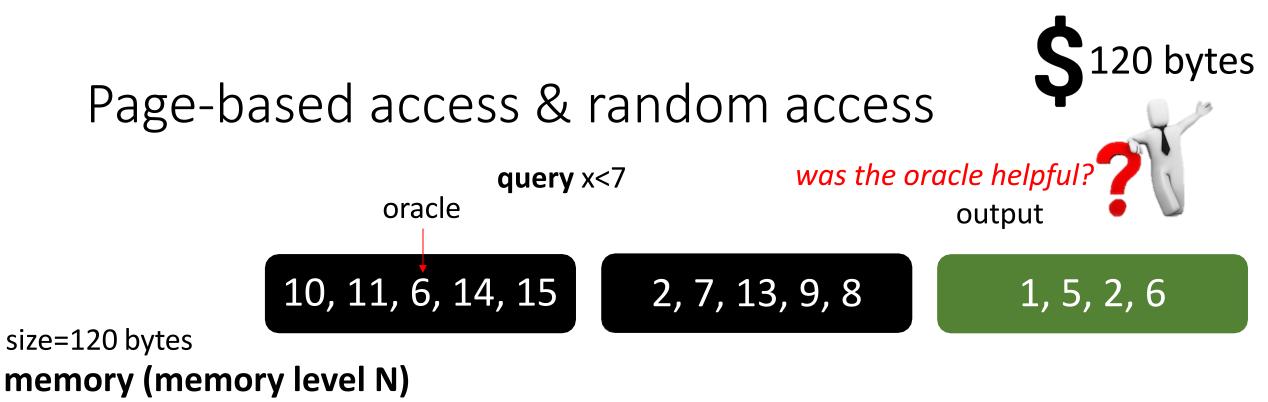




memory (memory level N)

disk (memory level N+1)





disk (memory level N+1)



When is the oracle helpful?





for which query would an oracle help us?

how to decide whether to use the oracle?



how we store data

layouts, indexes

every **byte** counts

overheads and tradeoffs

know the query

access path selection

index design space



Rules of thumb

sequential access

read one block; consume it completely; discard it; read next;

hardware can predict and start prefetching

prefetching can exploit full memory/disk bandwidth

random access

read one block; consume it partially; discard it; (may re-use);

read random next;

ideal random access?

the one that helps us **avoid a large number of accesses** (random or sequential)



The language of efficient systems: C/C++ *why?*

low-level control over hardware

make decisions about physical data placement and consumptions

fewer assumptions



The language of efficient systems: C/C++ *why?*

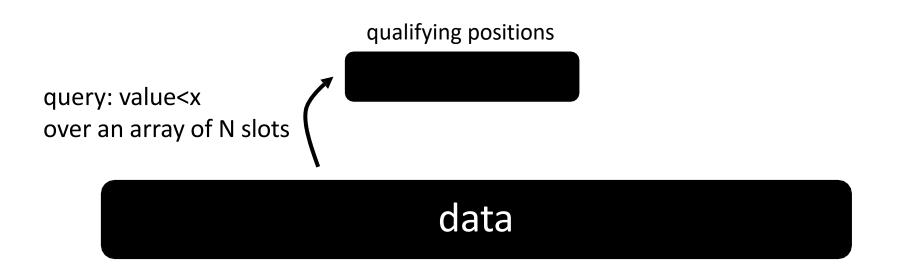
low-level control over hardware

we want you in the project to make low-level decisions

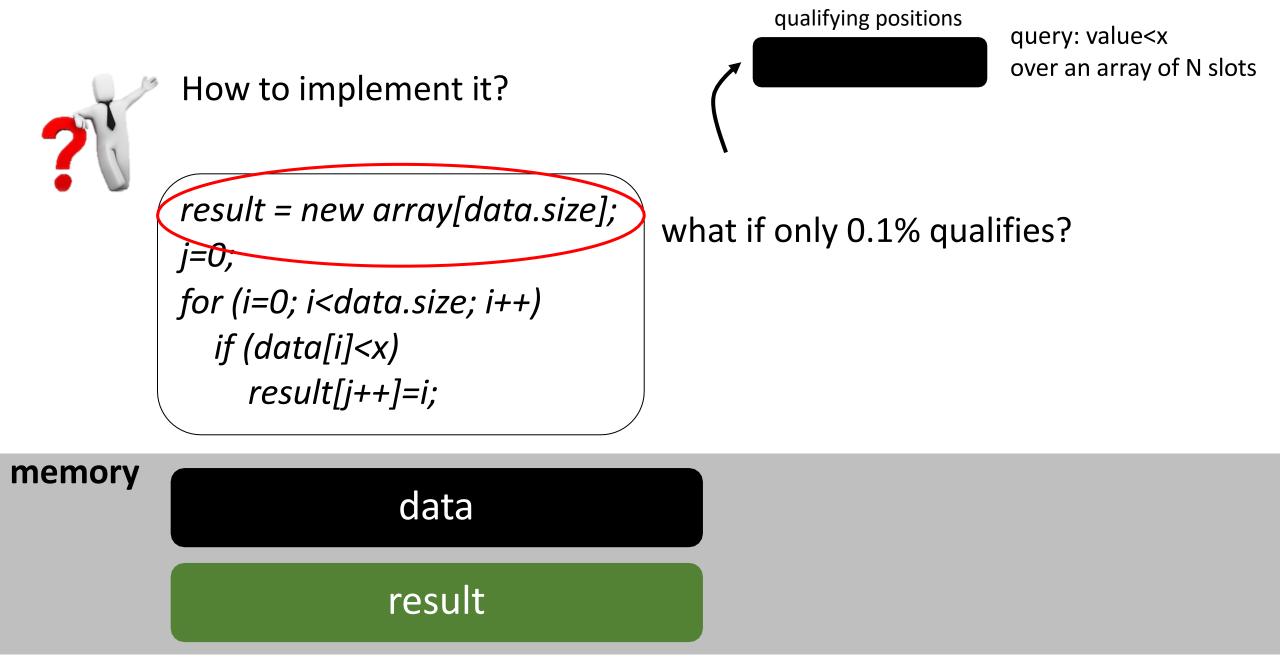


main-memory optimized-systems A "simple" database operator

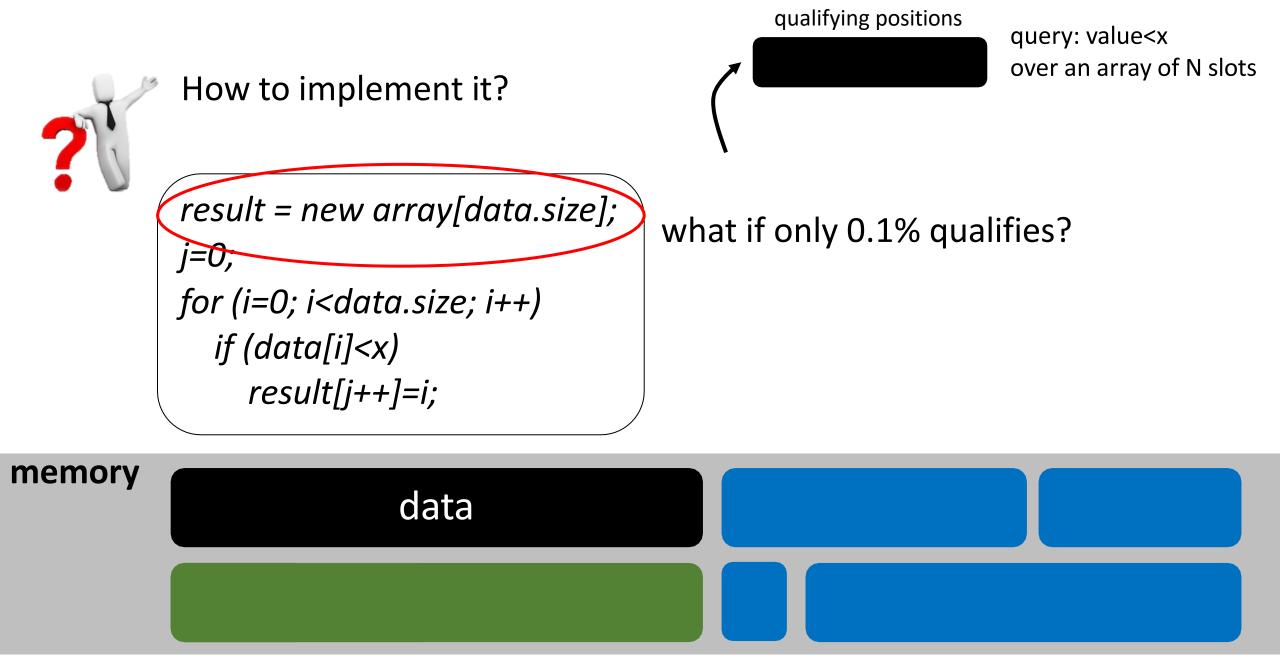
select operator (scan)



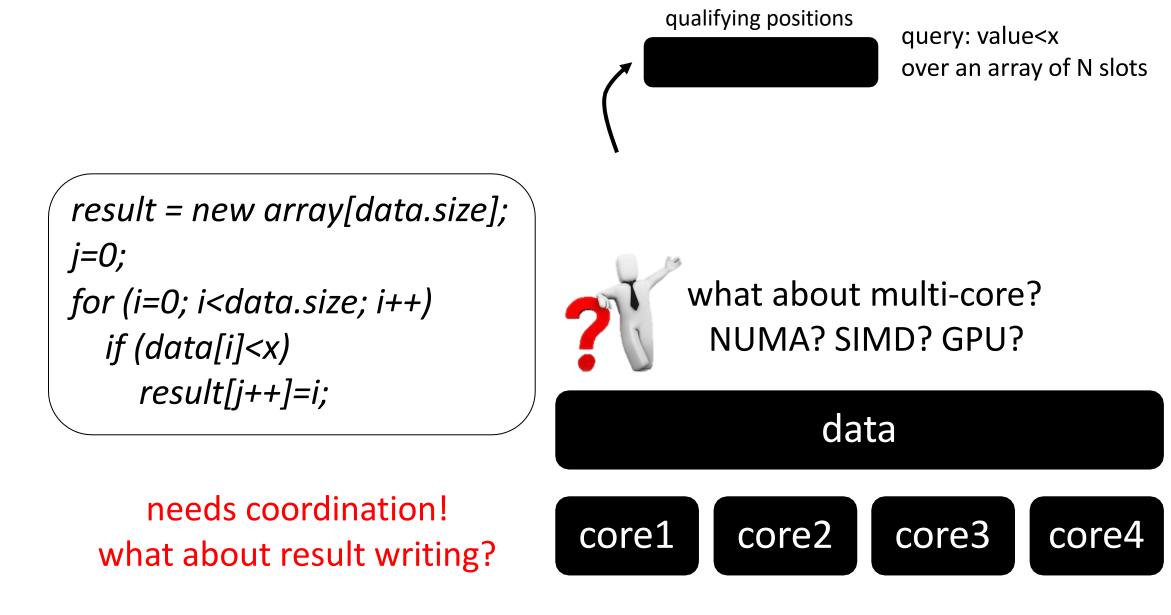




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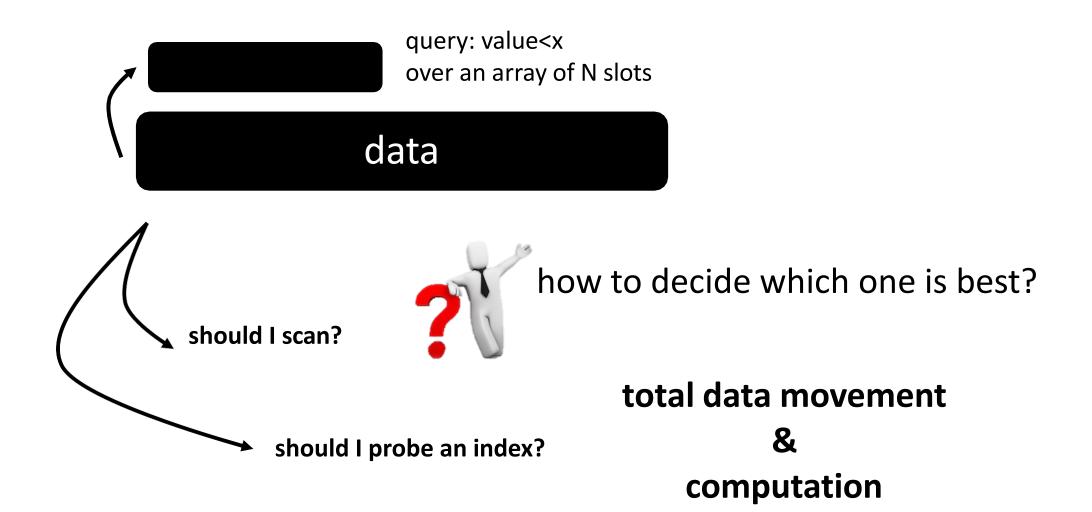
What about having multiple queries?

result = new array[data.size];
j=0;
for (i=0; i<data.size; i++)
 if (data[i]<x)
 result[j++]=i;</pre>

query1: value<x1
query2: value<x2 ...</pre>









Next class

In-memory indexing

Make sure to read the related papers from the reading list

