

# Using Intel Software on VS2008

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- File -> New Project (C++)
- Right Click Project or Project -> Intel Compiler -> Use Intel Compiler
- Project -> Properties -> C/C++ -> General -> Suppress Startup Banner = No
- Project -> Properties -> C/C++ -> Optimization -> Maximize Speed (/O2)
- Project -> Properties -> C/C++ -> Optimization -> Enable Intrinsic Functions (/Oi)
- Project -> Properties -> Code Generation -> Runtime Library -> Multithreaded DLL (/MD)
- Project -> Properties -> Code Generation -> Enable Enhanced Instruction Set = Streaming SIMD Extensions 3 (/arch:SSE3)
- Project -> Properties -> Command Line -> Additional Options -> Add /Qvec-report:3
- Click Apply
- Your command line options should look like

```
/c /O2 /Oi /D "WIN32" /D "_DEBUG" /D "_CONSOLE" /D  
"_UNICODE" /D "UNICODE" /EHsc /MD /GS /arch:SSE3 /fp:fast /  
Fo"Debug/" /W3 /ZI /Qvec-report:3
```

# Project 1 Assignment

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- **PART I**

- Each of the files t1.c, t2.c, t3.c, t4.c and t5.c from the website cannot be vectorized by the ICC compiler. Your assignment is to produce the equivalent but vectorizable nt1.c, nt2.c, nt3.c, nt4.c and nt5.c. To determine whether or not the compiler has vectorized a loop in the code, look at the output of the compiler that results from the flag -vec-report3. If it says: "remark: LOOP WAS VECTORIZED.", then you have succeeded! Hints: There are several reasons why the above examples cannot be vectorized. In some cases, the compiler is concerned about the efficiency of the vectorized code. This may be because of the cost of alignment, concerns about exceeding the register capacity (there are only 8 128-bit registers on most SSE3-supporting platforms!) or the presence of data dependences. In other cases, there is concern about correctness, due to aliasing.

# Project 1 Assignment, cont.

## • PART II

The code example `youwrite.c` is simplified from a sparse matrix-vector multiply operation used in conjugate gradient. In the example, the compiler is able to generate vectorizable code, but it must deal with alignment in the inner loop. In this portion, we want you to be a replacement for the compiler and use the intrinsic operations found in Appendix C (pp. 832-844) of the document found at:

<http://developer.intel.com/design/pentiumii/manuals/243191.htm>.

The way this code will be graded is from looking at it (no running of the code this time around), so it would help if you use comments to describe the operations you are implementing. It would be a good idea to test the code you write, but you will need to generate some artificial input and compare the output of the vectorized code to that of the sequential version.

## Homework 2, Problem 1 Solution

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Problem 1 (#10 in text on p. 111):

The Red/Blue computation simulates two interactive flows. An  $n \times n$  board is initialized so cells have one of three colors: red, white, and blue, where white is empty, red moves right, and blue moves down. Colors wrap around on the opposite side when reaching the edge.

In the first half step of an iteration, any red color can move right one cell if the cell to the right is unoccupied (white). On the second half step, any blue color can move down one cell if the cell below it is unoccupied. The case where red vacates a cell (first half) and blue moves into it (second half) is okay.

Viewing the board as overlaid with  $t \times t$  tiles (where  $t$  divides  $n$  evenly), the computation terminates if any tile's colored squares are more than  $c\%$  one color. Use Peril-L to write a solution to the Red/Blue computation.