



# Computer Science Graduate Handbook

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Department of Computer Science  
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**1999–2000, Revised**

Founded in 1965, the Department of Computer Science offers highly-regarded programs at both the undergraduate and graduate levels. Department faculty and students have done pioneering work in interactive graphics, stack machine and dataflow architectures, digital recording, graphical user interfaces, three-dimensional rendering, asynchronous circuits, video games, computer algebra, and computer animation. Faculty and alumni have also founded a number of well-known companies, including Adobe Systems, Ashlar, Atari, Cirrus Logic, Evans & Sutherland, Myricom, Netscape, Pixar, Pixel-Planes, Silicon Graphics, and Word Perfect.

Graduate students immerse themselves in the research activities of the department, which currently include:

- asynchronous VLSI systems
- compilers
- computer architecture
- computer vision
- educational computing
- high-speed GaS circuits
- human-computer interaction
- natural language processing
- operating systems
- programming languages
- scientific computing and visualization
- software development tools
- structured VLSI design
- automated knowledge acquisition
- computer-aided geometric design
- computer graphics
- databases
- formal methods for system design
- geometric modeling
- information-based complexity
- numerical analysis
- parallel and distributed computing
- robotics
- security
- software engineering
- virtual environments

These research activities are funded from a variety of federal, state, and industrial sources, including the National Science Foundation (NSF), the Defense Advanced Research Projects Agency, the Department of Energy, the Office of Naval Research, the National Institutes of Health, the Utah State Centers of Excellence Program, Silicon Graphics, Hewlett-Packard, and IBM. The department is a partner in an NSF Science and Technology Center for Computer Graphics and Visualization along with Brown, Caltech, Cornell, and the University of North Carolina; it recently received an NSF Research Infrastructure award to support research activities requiring high-bandwidth, low-latency machine-to-machine communications; it is a partner in the DOE Advanced Visualization Technology Center along with Los Alamos National Laboratory and

Argonne National Laboratory; and it is a key participant in the University's \$20 million Accelerated Strategic Computing Initiative grant from the Department of Energy.

Graduate students have access to hundreds of Unix and Windows NT workstations and to the more specialized equipment that resides in the various research laboratories. This equipment includes a 96 CPU SGI Origin 2000 with 8 Infinite Reality Engines; SGI ONYX2, Power Challenge, Power Onyx, and Origin 200 computers; robot arms, mobile robots, and image digitization and display systems; a variety of visual and non-visual virtual environment interfaces; a professional-quality video editing and teleconferencing facility; advanced graphics display workstations equipped with special-purpose graphics hardware; and a collection of numerically controlled equipment used to produce physical prototypes of computer-generated designs.

The University of Utah is committed to policies of equal opportunity, affirmative action, and nondiscrimination. The University seeks to provide equal access to its programs, services, and activities for people with disabilities. Reasonable prior notice is needed to arrange accommodations.

(This handbook is available online at <http://www.cs.utah.edu/csinfo/handbooks>.)

# Faculty

<b>Professors</b>	Elaine Cohen, Ph.D. Thomas C. Henderson, Ph.D. John M. Hollerbach, Ph.D. Robert Kessler, Ph.D. Kent F. Smith, Ph.D. William B. Thompson, Ph.D.	Alan L. Davis, Ph.D. Lee A. Hollaar, Ph.D. Gary E. Lindstrom, Ph.D. Richard F. Riesenfeld, Ph.D. Frank Stenger, Ph.D.
<b>Clinical Professors</b>	David Hanscom, Ph.D.	Joseph L. Zachary, Ph.D.
<b>Research Professor</b>	Stephen Jacobsen, Ph.D.	
<b>Emeritus Professor</b>	Robert R. Johnson, Ph.D.	
<b>Associate Professors</b>	Erik Brunvand, Ph.D. Charles Hansen, Ph.D. Peter Shirley, Ph.D.	Ganesh Gopalakrishnan, Ph.D. Christopher R. Johnson, Ph.D. Kris Sikorski, Ph.D.
<b>Research Associate Professor</b>	Sam Drake, Sc.D.	
<b>Assistant Professors</b>	John Carter, Ph.D. Ellen M. Riloff, Ph.D.	Wilson Hsieh, Ph.D.
<b>Research Assistant Professors</b>	Matthew Flatt, Ph.D. Sally McKee, Ph.D. Brian Smits, Ph.D.	Jay Lepreau Chris Myers, Ph.D.
<b>Adjunct Professor</b>	Martin Griss, Ph.D.	
<b>Adjunct Associate Professors</b>	Robert McDermott, Ph.D.	
<b>Adjunct Assistant Professor</b>	Don Brown, Ph.D.	
<b>Visiting Assistant Professor</b>	Rudiger Westermann, Ph.D.	



# Administration

Robert R. Kessler	Chair	581-4653
Ganesh Gopalakrishnan	Associate Chair, Research	581-3568
Joseph L. Zachary	Associate Chair, Academics	581-7079
Charles Hansen	Director, Graduate Studies	581-3154
David Hanscom	Director, Computer Engineering	581-7023
	Director, Undergraduate Studies	
Christopher R. Johnson	Director, Computational Engineering and Science	581-7705
Joseph L. Zachary	Director, Educational Programs	581-7079

# Staff

John Allen-Rose	Computer Professional	581-8065
Mark Bradakis	Computer Professional	585-3358
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Chaiyen “Amy” Liu	Hourly Clerk	581-8224
Amber Christensen	Clerical Assistant	581-8224
Jim Conforti	Computer Professional	585-1761
Shawn Darby	Administrative Manager II	581-3950
Erin Davies	Administrative Secretary	581-8224
Steve Dean	Computer Professional	585-9185
Karen Feinauer	Administrative Secretary (Part-time)	581-4132
Todd Green	Computer Administrator	581-3569
Mark Hartner	Computer Operator	585-9047
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Chad Lake	Computer Professional	581-1870
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Scott Ostrander	Computer Administrator	
Abbey Talbot	Clerical Assistant	581-8224
Sarah Walsh	Computer Technician	585-9047
Raelynn Potts	Administrative Officer	585-5983



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# 1

## M.S. and Ph.D. Admissions

The Department of Computer Science conducts an active and well-funded research program, which allows it to provide high-quality graduate education and research experience to a select group of creative and highly motivated graduate students. Approximately twenty-five new graduate students enter the program annually, roughly evenly divided between the M.S. and Ph.D. programs. Most graduate students are supported financially throughout their graduate career via a combination of teaching assistantships (TAs) and research assistantships (RAs). Our admissions standards are high and competition for the limited number of positions in the program is rigorous. Admission is based on an evaluation of both an applicant's *academic profile* and *research potential*. We especially encourage applications from underrepresented minorities and women.

### 1.1 Required Application Materials

The following materials are required of **ALL** applicants to either the M.S. or Ph.D. programs, including those wishing to transfer into the program from another department at the University of Utah:

1. **Application Form:** Complete and return the Department of Computer Science Application for Admission to Graduate School. There is no application fee for the computer science application form. However, once a student is accepted into our graduate program and decides to attend, the student is then required to submit another application form to the University of Utah. The application fee for the university is \$40 for domestic students and \$60 for international students.
2. **Grade Reports and Transcripts:** In accordance with the instructions on the application form, arrange to have two copies of your transcripts sent to the Department of Computer Science directly from the issuing schools.
3. **GRE Aptitude and Advanced Test in Computer Science:** All persons interested in applying to the Department of Computer Science at the University of Utah are required to take the GRE General and *an appropriate Subject exam* and have their scores forwarded to the department. *In almost all cases, the subject exam should be in Computer Science. Applicants wishing to submit subject exam scores from a different area should obtain prior approval from the department. Applicants who are deficient in formal computer science courses may request permission to substitute a related advanced test.* Students should plan far enough ahead to accommodate this requirement. Specifically, applicants must take the GRE exam sufficiently early to allow their scores to arrive by the application deadline.

4. **Letters of Recommendation:** Arrange to have three letters of recommendation sent directly to the Department of Computer Science *Graduate Coordinator*. We strongly prefer that all recommendations be from current or former professors who have a knowledge of the applicant's abilities through classwork, independent study, and/or research. However, a letter from a professional supervisor is acceptable if the supervisor is from a computer-related field and is qualified to judge the academic and technical capabilities, character, and ability of the applicant to perform research. *It is helpful if letters for non-native English speakers address the applicant's English skills.*
5. **Personal Letter:** Send a one- to two-page letter to the Department of Computer Science *Graduate Coordinator*, describing in depth your background, interests, and in particular, your reasons for wanting to pursue graduate studies in computer science at the University of Utah.
6. **TOEFL Scores:** Applicants whose native language is not English must take the TOEFL (Test of English as a Foreign Language) and have the score reported to the Department of Computer Science.

**Note for foreign applicants:** Letters of recommendation and your own personal letter must be written in English.

## 1.2 Application Deadline Dates

*Applicants are normally evaluated for admission effective Fall Semester.* Such applications must be received at the University of Utah by January 15 of the academic year prior to the desired start of studies. Exceptionally well qualified students may be admitted starting Spring Semester with an application deadline of October 1. Applications for entry effective Summer Term are not accepted.

## 1.3 Addresses

Applicants should have *all* application materials (application form, transcripts, GRE scores, letters of recommendation, TOEFL scores (if applicable), and personal letter) sent directly to the Department of Computer Science at the address below. Questions regarding the status of an application or regarding the computer science graduate program in general should be sent to the Graduate Coordinator.

Graduate Coordinator  
University of Utah  
Department of Computer Science  
50 S. Central Campus Drive, RM 3190  
Salt Lake City, UT 84112-9205 USA  
grad-coordinator@cs.utah.edu  
(801) 581-8224

## 1.4 If You Are Accepted

Once you have received your letter of acceptance from the Department of Computer Science and decide to attend, be sure to notify the Department of Computer Science in writing regarding your intention to matriculate. You will then be required to submit another application form to the University of Utah. The application fee for the university is \$40 for domestic students and \$60 for international students. If you are accepted with financial aid, obtain a TA/RA Request form from the Graduate Coordinator and return it promptly.

International Teaching Assistants (TAs who are not native speakers of English) must take either a Test of Spoken English (TSE) or SPEAK test before beginning their first TA assignment. Depending on exam score, remedial instruction at the

University's English Language Institute may be required. International Teaching Assistants are required to participate in the ITA Workshop prior to their first semester. This is typically held during the two weeks prior to start of the semester.

## 1.5 Frequently Asked Questions

### What kind of academic background is required?

Neither of the graduate programs (M.S. or Ph.D) is an entry-level degree. Although it is not necessary that you have a B.S. in Computer Science, both degree programs assume a background with knowledge in:

- Programming languages
- Algorithms and data structures
- Operating systems
- Computer system organization
- Machine architecture
- Logic design
- Discrete mathematics

### Is there a GPA cutoff?

Yes, a minimum GPA of 3.0 in undergraduate work is required. Most students accepted into the graduate program have GPAs well above that level.

### Is the GRE required?

Yes, all applicants are required to take the GRE General and *an appropriate Subject exam*. Because our graduate programs are not entry-level, we strongly prefer that the Subject exam be in Computer Science in order to help us judge an applicant's computer science background. *In truly exceptional circumstances, we may allow an alternate Subject exam to be substituted. Anyone wishing to submit scores from an area other than computer science should consult with the Department's Director of Graduate Admissions prior to submitting his or her application.*

### Is there a TOEFL cutoff?

Yes, 620 is the minimum acceptable score. In addition, any non-native English speaker who receives support as a teaching assistant must attend and pass the appropriate University programs in spoken English.

### What is the application fee? Can it be waived?

There is no application fee for the Department of Computer Science. However, students who are accepted into the computer science graduate program and decide to attend, must then submit an application form to the University of Utah. The university application fee is \$40 for domestic applicants and \$60 for foreign applications. It cannot be waived under any circumstances.

### Is there anything else involved in the admission process besides sending my application to the Department of Computer Science?

Yes, you must supply the department with three letters of recommendation, academic transcripts, a personal statement, official GRE scores, and TOEFL scores (if applicable). Your application cannot be processed by the Department Graduate Admissions Committee until it is complete.

**What are you really looking for in an applicant?**

*We are looking for applicants with strong academic backgrounds who have demonstrated a potential to perform creative and innovative research in computer science.*

**Who should I contact if I have any questions?**

Send e-mail to [grad\\_info\\_questions@cs.utah.edu](mailto:grad_info_questions@cs.utah.edu).

**Can I get more information on the Department or copies of the admissions forms electronically?**

Yes. You can get copies of this handbook and all of the admissions forms as well as detailed information about individual researchers' interests, ongoing research projects, and recent technical papers via the department's World Wide Web (WWW) server, located at <http://www.cs.utah.edu>.

## **1.6 Transfers Within the CS Graduate Program**

The following section details departmental policy and procedures for currently matriculated computer science graduate students who wish to change degree programs (M.S. to Ph.D. or vice versa), and students who are about to complete a degree program and wish to continue in another. All such applications will be reviewed by the Graduate Admissions Committee to ensure standards consistent with those applied to outside applicants. The same admissions deadlines apply as for outside applicants (i.e., a student wishing to transfer from the M.S. program for the Fall Semester must submit their application by January 15).

### **1.6.1 Transfer into the Ph.D. Program**

A student wishing to transfer to the Ph.D. program must submit the following material to the Graduate Admissions Committee:

1. A "goal letter" explaining the student's motives and describing the intended research area.
2. Letters from three current department faculty members supporting the application. In the event that the applicant's supervisory committee has been formed, then at least one of these letters must be from the chair of that committee.
3. A University of Utah transcript.

Applications will not be reviewed unless at the time of application the applicant has completed at least two academic semesters of graduate study. Summer term does not count in this tally.

### **1.6.2 Transfer into the M.S. Program**

Students currently enrolled in the Ph.D. program who wish to transfer to the M.S. should make their request in writing to the Director of Graduate Studies. Supporting letters from three faculty members expressing willingness to serve on the student's M.S. Supervisory Committee must also be provided.

# 2

## Information for Graduate Students

The following summarizes the formal requirements and procedures of the M.S. and Ph.D. programs. Other pertinent information can be obtained from the bulletin of the Graduate School of the University of Utah.

Many aspects apply to both the M.S. and Ph.D. degree programs in Computer Science. Information that applies to both M.S. and Ph.D. students is presented first, followed by a description of requirements specific to each degree program.

Please see the Graduate Coordinator (currently, Colleen Hoopes) for additional material regarding administrative guidelines, procedures, and help. It is the responsibility of the student to initiate processing the various forms; however, the department and the graduate school will try to help you in any way possible.

### 2.1 The Graduate Studies Committee

The functions of the Graduate Studies Committee are as follows:

1. Oversee the graduate curriculum and make timely recommendations to the faculty on its findings.
2. Execute the departmental graduate policy on admissions and financial support.
3. Make teaching assistantship assignments.
4. Monitor the progress of all graduate students in the Department. This monitoring relies on annual progress reports from students and their advisors (supervisory committee chairs). The advisor of a student whose progress is questionable will be invited to discuss the student's situation with the Committee. Appropriate actions according to accepted departmental guidelines result from the student reviewing process.
5. Recruit new graduate students.

During 1999–2000, the Committee consists of Professors Charles Hansen (chair), Ellen Riloff, and Wilson Hsieh.

Information specific to a particular semester, such as desk assignments and comprehensive exam dates, are on the graduate studies web page: <http://www.cs.utah.edu/grad-studies>.

## 2.2 Financial Assistance For M.S. and Ph.D. Students

### Assistantships and Fellowships

There are three types of financial aid available to graduate students in the Department of Computer Science. Teaching and research positions are awarded on a semester-by-semester basis. Teaching assistantships are provided by the department, while research assistantships are awarded by individual faculty serving as investigators on research grants and contracts. A third form of support is research fellowships (including traineeships), the terms of which vary.

The duties and benefits of departmental teaching and research assistantships are defined as follows:

1. *Teaching Assistantship*: A teaching assistant is a matriculated student employed 10 to 20 hours per week to assist a faculty member in teaching. The teaching assistant is required to meet with students regularly in a classroom, laboratory, or other instructional setting; to assist in instructional duties through lesson and materials preparation; to counsel students outside of the regularly scheduled instructional periods; and to evaluate and grade students' work to aid in the determination of final course grades. Tuition waivers supplement salary support.
2. *Research Assistantship*: The Director of Graduate Studies assigns each research assistant to a particular faculty member based on mutual agreement of the faculty and student. The duties assigned to a research assistant are consonant with the student's research interests and also useful to the professor's research efforts. A research assistantship can be viewed as an internship, whereby the student learns by practicing under faculty supervision. A student wishing to be a research assistant should inquire directly with appropriate faculty sponsors. Tuition waivers supplement salary support.

Continuation of financial aid is dependent upon continued competent performance of teaching or research duties, as well as satisfactory progress in the student's program of study.

Research fellowships, such as those awarded by national foundations, can be requested directly from the granting agency under the supervision of the Director of Graduate Studies. In addition, the department annually nominates outstanding graduate students for University Research Fellowships, as well as fellowships for certain categories of graduate students from private corporations and federal agencies. In the past students in the department have been awarded fellowships from NSF, DOE, DARPA, ACM, AMOCO, Apple, ARO, ONR, and IBM.

### Summer Support

Faculty members who are conducting research projects often hire students as research assistants on a half or full-time basis during the summer. Students interested in a summer appointment as a research assistant should make arrangements directly with the appropriate faculty member. There are a few teaching assistantships available during the Summer Semester.

### Graduate Assistantship Duration and Pay Scales

Graduate student support is contingent upon satisfactory progress toward degree completion. The number of assistantships in each semester depends upon available funding. The department will notify students of appointments as early as possible before the start of a semester. It is usually not possible, however, to notify students of their specific assignments (that is, whether they will be an RA or TA) until the beginning of each semester, as these decisions are based on class enrollment figures. The table below gives the current pay scale for Computer Science graduate RAs and TAs at the customary 20 hour/week level. Undergraduate TAs are paid on a separate hourly scale.

Description	Level of Support
Student in M.S. program	\$1,350/mo
Student in Ph.D. program (pre-proposal)	\$1,350/mo
Student in Ph.D. program (post-proposal)	\$1,450/mo

Full-time students who are employed by the University are exempt from FICA (Social Security) taxes, and should file a form to this effect. This form is available from the department administrator or from the Payroll office in 103 Park Building. A form must be filed *each* semester to keep the exemption in effect.

### Tuition Waivers

Tuition waivers are available to Teaching and Research Assistants working 20 hours/week or more. Tuition benefits are applicable to all courses numbered 5000 and above that contribute to the student's degree course requirements. Students must take between 9 and 12 semester hours to receive a full tuition waiver.

By Graduate School policy, tuition waivers are limited to four semesters for M.S. students. Students entering the Ph.D. program with a master's degree in Computer Science are limited to six semesters of tuition waiver support. Ph.D. students entering without a master's are limited to ten semesters of tuition waiver support. See the Graduate School Bulletin for additional requirements.

### Procedures for Applying for Financial Assistance

Qualified full-time graduate students are eligible for financial aid in the form of teaching assistantships or research assistantships as described above. For incoming students, a financial assistance application form can be obtained in the Department of Computer Science as well as from the Graduate Fellowship Office, 312 Park Building. A completed application should be submitted directly to the Department of Computer Science by February 1 for aid to commence the following Fall Semester.

## 2.3 Transfer Credit

A student may not count more than eight semester hours of non-matriculated graduate work toward any graduate degree unless the student's registration for more than eight semester hours is specifically approved in advance by the department chair and the dean of the Graduate School. Graduate courses taken as an undergraduate at the University of Utah cannot be counted towards a degree program unless a petition for graduate credit was filed with the University's Registrar at the time the course was taken.

Students who have done graduate study at other institutions may apply appropriate course work to their program at the University of Utah. The following guidelines apply:

1. The courses must be *bona fide* graduate level class work (e.g., independent study is excluded), with grade B- or better. The course work must have been taken on a postgraduate basis, i.e., not have been used to fulfill Bachelor's degree requirements.
2. Credit may be given to M.S. students for up to six semester hours, and up to 20 semester hours for Ph.D. students. Quarter hours are converted to equivalent semester hours by a .66 multiplicative factor (e.g., 9 quarter hours = 6 semester hours).
3. Approval of each course is granted by the student's supervisory committee. Course appropriateness is determined by consideration of course content and the student's declared research area.

4. Approved courses are certified by inclusion on the student's *Application for Admission to Candidacy for the M.S. Degree* or *Program of Study for the Ph.D. Degree*. (Henceforth, both these forms will be uniformly referred to as "Program of Study" forms.) These must be submitted by the end of the student's second semester of study.
5. Approval of a course taken elsewhere for transfer credit does not imply fulfillment of any specific required course. The waiving required courses can be petitioned for using a form available from the Graduate Coordinator. Normally, the Utah professor who most recently taught the required course determines whether petition should be granted. This can be done at any time, e.g., prior to filing the Program of Study form. Petitions may be granted even if the course was applied to complete Bachelor degree requirements (see point 1).
6. For Ph.D. students:
  - (a) Approval of courses taken elsewhere has no effect on comprehensive and qualifying exam requirements. Some students may elect to complete a course at Utah even though they may have taken a comparable course elsewhere, to aid in preparation for these exams.
  - (b) Approval of courses taken elsewhere has no effect on University of Utah residency requirements (see Section 2.10).

## 2.4 Thesis/Dissertation Requirements

The M.S. degree requires a thesis, and the Ph.D. degree a dissertation, to be completed and successfully defended before the degree can be awarded. (In the discussion that follows, the term "thesis" is used to refer to both.) The supervisory committee must give preliminary approval of the thesis prior to the defense. The thesis defense can be scheduled after this approval. To schedule the defense, contact the Graduate Coordinator. The student must provide one copy of the thesis to the chair of the supervisory committee at least three weeks before the thesis defense, and one copy to each of the other committee members at least two weeks prior to the defense. A complete draft of the thesis must be delivered to the Graduate Coordinator one week prior to the announced time of defense. This copy will be made available for public access. Students are encouraged to place an additional copy on the CS Department web pages at least one week prior to the announced time of defense. Students are expected to offer each committee member a bound copy of the thesis once it is completed.

After successfully defending the thesis, the student must obtain approval from the Final Reader (typically the supervisory committee chair), department chair, and Dean of the Graduate School. A draft of the final thesis must then be presented to the Thesis Editor. Successful completion of the thesis defense must be reported to the Graduate School at least four weeks before the last day of examinations in the final semester. Students should also read the document regarding copyright notices provided by the department and declare their intentions regarding granting the department the right to photocopy the thesis before notifying the Graduate Coordinator of completion of the thesis defense.

The student has one month after the defense to make any revisions prior to submitting the thesis to the Graduate School Thesis Editor. There will be at most two additional months to complete any changes required by the Thesis Editor before final acceptance. If either of these deadlines are not met, the candidate must redo the oral defense of the thesis. The final thesis must be filed one week before the end of the semester of graduation.

## 2.5 Thesis Copyright Policy

The Department of Computer Science has a fiduciary interest in seeing that the results of its research programs are widely disseminated so that other researchers and the public as a whole can benefit. This is especially true for work supported in part by governmental agencies. Copyrighting of a thesis, without making any provision for its legal reprinting, severely limits the dissemination of research results. It also makes it difficult for the department to handle requests for copies of theses received from other universities or researchers.

The Department of Computer Science encourages distribution of theses and dissertations in any of the following ways:

- Through an appropriate agreement with University Microfilms, Inc., that allows them to reproduce the thesis on request (this option is only open to Ph.D. dissertations at the present time).
- Give permission to the department to have the thesis reproduced upon request, with the department allowed to recover its costs of reproduction and distribution.
- Furnish the department with the address of the person, agency, or company that will handle the distribution of the thesis.

## 2.6 Leave of Absence Policy

If a student does not plan to take classes during a fall or spring semester, a leave of absence must be requested. Contact the Graduate Coordinator for the proper form.

## 2.7 GPA Requirements

Students are required to meet minimum grade and GPA requirements for their classes. Classes on the Program of Study must have a course number of 5000 or above. Students are required to earn a grade of B- or better in every class on the Program of Study, and must maintain an overall GPA of at least 3.0 in those classes. Students are required to earn a grade of B or better in any required class, and achieve an overall GPA of 3.5 in required classes.

## 2.8 Course Load

Graduate School policy dictates that a graduate student who receives a full tuition waiver during any semester in which he or she holds an assistantship, fellowship or traineeship is required to register for at least nine semester hours, including thesis research and seminars.

All teaching assistants, as well as students receiving fellowship or traineeships stipends, are required to register for CS 6930 (Computer Science Seminar) for one semester hour.

Students must be registered for at least three hours per semester, exclusive of summer semester, in order to remain in a graduate degree program. Students who do not maintain continuous registration and who have not been granted a leave of absence by the Graduate School are subject to being discharged from the degree program. Students must be registered for at least three semester hours during the semester of the student's thesis defense.

Normally, nine semester hours per semester is considered a full load for graduate students. Students being supported via research or teaching assistantships who wish to receive tuition waivers must register for between nine and twelve hours per semester, as described above.

## 2.9 The M.S. Degree

The M.S. is a research degree offered through the Graduate School. A student who has been accepted by the Graduate School is formally admitted to candidacy for the M.S. degree at the recommendation of the student's supervisory committee. Admission to candidacy occurs after the student:

- Forms a supervisory committee,
- Files an approved Program of Study form,
- Passes the comprehensive examination, and
- Submits an approved thesis proposal.

An application for candidacy must be submitted to the Graduate School no later than the last day preceding the semester of graduation. For the degree to be conferred, the approved Program of Study form must be completed and the thesis completed and publicly defended.

Each of these steps is described below. Most of the steps involve completing and submitting a properly signed form. Forms and assistance are available from the Graduate Coordinator.

**Supervisory committee.** An M.S. committee consists of three members. A committee typically consists of departmental faculty, but may include qualified external members. The committee should be formed by the second semester of enrollment in the M.S. program.

Any computer science regular faculty member may serve as a supervisory committee chair. Other faculty may chair supervisory committees if accorded that privilege by the regular faculty. Individuals who are not faculty members may serve on supervisory committees if nominated by the regular faculty on the committee, and endorsed by the Graduate Studies Committee and department chair.

Final approval of all supervisory committees must be granted by the Dean of the Graduate School. M.S. students must form this committee by the end of the second semester of study, although a committee may be revised later by petition to the Graduate Studies Committee.

**Required Courses.** The Computer Science M.S. program is not considered an “entry level” program; therefore, all incoming students will be expected to have demonstrated a basic understanding of fundamental concepts. Students with non-traditional backgrounds must show proficiency in or complete the courses listed as “background” in Figure 2.1.

The curriculum requirements for M.S. students are designed to ensure that all students who receive a M.S. degree have a working knowledge of those topics in computer science that are deemed fundamental by the faculty. This comprises a basic education in the core areas of computer science and a deeper education in one or more areas in which they will perform research. A core area illuminates the ways that most applications are designed and implemented, examines the hardware and software systems that are needed to execute the applications, and analyzes the resulting performance of such applications. M.S. students are required to complete courses in each of these areas.

Figure 2.1 gives the *background* and *required* courses in each core area for M.S. students. It is assumed that all graduate students will have a thorough understanding of the material covered in the background courses. All M.S. students must take the required courses listed above, unless a waiver is obtained based on prior knowledge (e.g., completion of a similar course taken at another University). M.S. students are required to earn a grade of B or better in each required class, and achieve an overall GPA of 3.5 in those classes.

**Program of Study.** Course work listed on the approved Program of Study form must consist of at least 30 semester hours of graduate course work and thesis research. At least 6 semester hours of thesis research (CS 6970) and 20 semester hours of graduate course work must be included.

CS courses on the Program of Study must be numbered 6000 or above, excluding research credits (CS 6970); or they must be required courses. Of the required 20 semester hours, up to 6 may be in courses outside of CS. At least one course in the Program of Study must be a CS course numbered 6000 or above, excluding independent study, seminars, research credit, and required courses.

Area	Background (Prerequisites)	Required
<i>Foundations</i>	CS 3100 Models of Computation CS 3510 Algorithms and Data Structures	CS 6100 Foundations of Computer Science
	CS 3100 Models of Computation CS 3520 Programming Language Concepts	CS 6520 Programming Languages and Semantics
<i>Systems and Languages</i>	CS 3510 Algorithms and Data Structures CS 3810 Computer Architecture	CS 5460 Operating Systems
	CS 3100 Models of Computation CS 3510 Algorithms and Data Structures CS 3810 Computer Architecture	CS 5470 Compiler Principles and Techniques
<i>Architecture</i>	CS 3700 Digital System Design CS 3810 Computer Architecture	CS 6810 Advanced Computer Architecture

Figure 2.1: Course requirements for M.S. students in Computer Science

At least 24 semester hours must be completed in resident study at the University of Utah. Students must be registered for a minimum of 3 semester hours during the semester in which the thesis is defended.

Independent study (CS 6950 and CS 7950) can be included in the Program of Study for the M.S. degree. Independent study for M.S. students will be allowed only when the project is self-contained and independent of thesis research. Course CS 6944 (Computer Science Seminar) may not be applied to the course requirements of the M.S. degree program.

A student may register for CS 6020 if that student writes and publishes a peer-reviewed article based on research performed in the University of Utah Department of Computer Science. The contribution of the student to the article should be equivalent to that conferred by first authorship. The paper should be published in a respectable outlet. It is the responsibility of the student's advisor to determine whether the student has made such a contribution, and whether the outlet is of sufficient quality. This paper must be accepted for publication prior to the end of the second year of study.

The Program of Study form should be filed with the department in the second semester of study and with the Graduate School prior to taking the comprehensive examination. The Program of Study form must be submitted to the Graduate School by the last day of the semester preceding the semester of graduation.

**Comprehensive examination.** The comprehensive examination for M.S. students consists of an oral examination on the thesis proposal and research area in a very broad sense. This examination is administered by the student's supervisory committee and should be completed by the end of the student's third semester of study (not counting summers) as a graduate student in the department. The examination should serve as the defense of the student's thesis proposal as well as to establish competence in the research area.

**Thesis proposal.** The student should prepare and receive approval for a thesis proposal by the end of the third semester of study (not counting summers). A copy of the thesis proposal must be in the student's file. For guidelines on preparing proposals, consult *Discussion on Ph.D. Thesis Proposals in Computing Science*, by H. C. Lauer. Copies are available from the Graduate Coordinator and from the Thesis Editor.

**Completing program of study.** An M.S. student is expected to devote the necessary time to courses and research in order to make satisfactory progress toward the degree. Satisfactory progress includes personal participation in the research and teaching environment of the department on a day-to-day basis.

**Thesis.** A full time student working on an M.S. program is expected to complete the degree requirements within two calendar years. Beyond this period a student generally does not receive graduate financial support from the department.

In special circumstances, the student may continue the M.S. program for a third year but without financial support. A student must petition the Graduate Studies Committee to continue beyond the third year. The Graduate School limits M.S. programs to four years.

## 2.10 The Ph.D. Degree

The Ph.D. is a research degree offered through the Graduate School. It is awarded to a candidate who has demonstrated breadth in Computer Science in general, and depth in a research specialty within Computer Science. The latter is exhibited through the writing and defense of a dissertation that reports substantial original contributions in an approved area of research.

A student who has been accepted by the Graduate School is formally admitted to candidacy for the Ph.D. by the University at the recommendation of the student's supervisory committee. Admission to candidacy occurs after the student:

- Forms a supervisory committee,
- Files an approved Program of Study form,
- Passes the comprehensive examination,
- Passes the written portion of the qualifying examination,
- Passes the oral portion of the qualifying examination, and
- Submits an approved dissertation proposal.

An application for candidacy must be submitted to the Graduate School no later than the last day preceding the semester of graduation. For the degree to be conferred, the approved Program of Study form must be completed and the dissertation completed and publicly defended.

Each of these steps is described below. Most of the steps involve completing and submitting a properly signed form. Forms and assistance are available from the Graduate Coordinator.

**Supervisory Committee.** Each student forms a supervisory committee whose members guide the student's research program. The committee conducts the student's comprehensive examination, written qualifying examination, oral qualifying examination, and dissertation defense. A Ph.D. supervisory committee consists of five faculty members and must include a member from outside the Department of Computer Science. Any computer science regular faculty member may serve as a supervisory committee chair. Other faculty may chair supervisory committees if accorded that privilege by the regular faculty. Individuals who are not faculty members may serve on supervisory committees if nominated by the regular faculty on the committee, and endorsed by the Graduate Studies Committee and department chair.

Final approval of all supervisory committees is granted by the Dean of the Graduate School. Students must form this committee by the end of the second semester of study, although a committee may be revised later by petition to the Graduate Studies Committee.

**Required Courses.** The Computer Science Ph.D. program is not considered an "entry level" program; therefore, all incoming students will be expected to have demonstrated a basic understanding of fundamental concepts. Students with non-traditional backgrounds must show proficiency in or complete the courses listed as "background" in Figure 2.1.

There are no specific required courses for Ph.D. students. The comprehensive examinations ensure that all students who receive a Ph.D. degree have a working knowledge of those topics in computer science that are deemed fundamental by the faculty.

**Program of Study.** Course work listed on the approved Program of Study form must comprise at least 50 semester hours of graduate course work and dissertation research, exclusive of independent study. Graduate course work applied toward an M.S. degree may be included. At least 14 semester hours of dissertation research (CS 7970) and 27 semester hours of graduate course work must be included.

CS courses on the Program of Study must be at the 6000 level or above, excluding independent study, seminars, and research credits. Of the required 27 semester hours, up to 6 may be graduate courses outside of CS.

Neither courses CS 6930–CS 6944 (Computer Science Seminars) nor Independent study (CS 6950 and CS 7950) can be included in the Program of Study for the Ph.D. degree.

One year of study must be spent in full-time residency at the University (i.e., the student must enroll for a minimum of nine hours per semester for two consecutive semesters, summer optionally excluded). After the residency requirement is fulfilled, registration for three semester hours of CS 7970 (Ph.D. Dissertation Research) is considered a full load.

A student may register for CS 6020 if that student writes and publishes a peer-reviewed article based on research performed in the University of Utah Department of Computer Science. The contribution of the student to the article should be equivalent to that conferred by first authorship. The paper should be published in a respectable outlet. It is the responsibility of the student's advisor to determine whether the student has made such a contribution, and whether the outlet is of sufficient quality. This paper must be accepted for publication prior to the end of the second year of study.

The Program of Study form should be filed with the department in the second semester of study and with the Graduate School prior to taking the qualifying examination. The Program of Study form must be submitted to the Graduate Records Office no later than the last day of the semester preceding the semester of graduation.

**Comprehensive Examination.** The Ph.D. Comprehensive Examination tests for breadth of knowledge across the core areas of computer science. A core area illuminates the ways that most applications are designed and implemented, examines the hardware and software systems that are needed to execute the applications, and analyzes the resulting performance of such applications. To demonstrate their knowledge of these topics, Ph.D. students are required to successfully pass a comprehensive exam in each core area. While core areas will correlate closely with courses offered by this department, the material deemed essential to each topic shall be defined by a reading list, not the coverage of any given class.

The comprehensive exams will be given once a year in the Fall. All parts of the comprehensive examination must be passed prior to completion of the fifth semester of study, not including summer enrollment. Subject to this time limit, failed exams may be repeated without the need to re-take those parts previously passed. Entering students are allowed to take the exams in their first semester.

The comprehensive exam will be prepared and graded by a faculty committee. This committee will be appointed in the spring semester prior to the offering date of the exam for which they are responsible. The committee will complete all work involved in writing examination questions and updating the reading list before January 1 of the year for which they are responsible. The same committee will be responsible for grading completed exams the following fall.

The comprehensive exam will be administered as a closed book exam.

The comprehensive exam committee is responsible for insuring that the core area exams satisfy the following criteria:

- All questions should be answerable by a well prepared CS Ph.D. candidate who is not specializing in the topic area covered by the question.
- All questions should be answerable based on the reading list and background that can reasonably be assumed to be a prerequisite to understanding what is included in the reading list.
- Adequate time is available for completing all of the questions on the exam.

The comprehensive exam committee is responsible for determining a single score for the entire exam. It is important that this be based on an analysis of the overall quality of answers we expect from our Ph.D. candidates, not just on a numerical

average of individual question grades. In particular, the exam committee should insure that passing exams demonstrate adequate knowledge across all major areas covered by the exam:

- Hardware: VLSI, architecture
- Systems and languages: Operating systems, networking, compilers
- Foundations: Algorithms, basic theory, language semantics

Further details about the comprehensive exams and the current reading lists can be found on the graduate studies web page at <http://www.cs.utah.edu/grad-studies>.

**Qualifying Examination.** After passing the Comprehensive Examination, all Ph.D. students must pass a Qualifying Examination, as specified by the graduate school. The Qualifying Exam consists of two parts, a written examination covering the candidate's chosen area of specialization and an oral examination involving a defense of the candidate's written thesis proposal. Students should pass their qualifying exam, which includes receiving committee approval for their dissertation proposal, by the end of their sixth semester of study, not counting summer enrollment. A copy of the dissertation proposal will be retained in the student's departmental file. For guidelines on preparing proposals, consult "Discussion on PhD Thesis Proposals in Computing Science" by H.C. Lauer. Copies are available from the graduate coordinator and from the thesis editor.

The written portion of the Qualifying Examination will cover the candidate's general area of specialization in sufficient depth to demonstrate his/her preparation for conducting Ph.D. level research. Each member of the student's advisory committee will contribute one or more questions to this exam. The advisory committee will provide a written evaluation of this portion of the exam, including an indication of whether or not the student will be allowed to proceed to the oral portion of the Qualifying Examination.

The oral portion of the Qualifying Exam involves a defense of the candidate's written thesis proposal. At the advisory committee's option, it may also include follow-up questions relating to the written portion of the exam. All members of the candidates committee should certify that the proposal is ready to be defended prior to conduction the oral portion of the Qualifying Exam.

Consistent with the requirements of the Graduate School, the Qualifying Examination must be completed no less than two semesters prior to defense of the thesis.

**Completing program of study.** A Ph.D. student is expected to devote the necessary time to courses and research in order to make satisfactory progress toward the degree. Satisfactory progress includes personal participation in the research and teaching environment of the department on a day-to-day basis.

**Dissertation.** The completed dissertation must be published either in its entirety (through a legitimate publisher of the student's choice or through University Microfilms) or as one or more articles accepted for publication in approved scholarly journals. An abstract of each dissertation must be published in University Microfilms' Dissertation Abstracts International. Detailed policies and procedures concerning the dissertation are contained in "A Handbook for Theses and Dissertations" published by the Graduate School.

## Administration of the Ph.D. Comprehensive Exam

Each year, prior to the administration of the comprehensive examination, the faculty member in charge will publish the target date for completion of the grading process. It is the intent of the faculty that grades be made available to students taking the exam within three weeks of when it is administered.

The reading list can only be changed by vote of the faculty. In addition, a change to the reading list must take place before January 1 of the year in which the affected comprehensive exam is given. Note: it is the expectation of the faculty that significant changes to the reading list will be made at least one year in advance of the expected comprehensive exam.

Three graduate students who have completed the comprehensive examination, been nominated by GradSAC, and been approved by the DGS will be given the opportunity to comment on (at least) the final draft of the comprehensive exam. They will be given the final draft of the exam at least one month prior to the actual examination dates.

Examination books should be identified by a code not known to any member of the faculty. Separate codes should be used for each area exam. Graders should write all comments separately from the examination books. Candidates should remain identified only by code until a pass or fail determination is made by the Comprehensive Exam Committee for a given area exam.

All questions should be graded independently by two graders. Questions should be assigned grades using the following code: high pass (HP), pass (P), fail (F).

A grade of P- signifies the minimal acceptable performance expected from a Ph.D. student. An F+ indicates an answer that is partially correct, but not up to the standards we expect from our Ph.D. students. Pluses and minuses are allowed in scoring individual questions and whole exams, but not grades of the form HP-/P+ or P-/F+.

The comprehensive exam committee is responsible for determining a single score for each question. In the case of significant disagreements between the two graders, either a third grading should be done or the original graders should confer to see if they can reach consensus.

The Grad Studies Committee, in consultation with the Comprehensive Exam Committee, will make an overall pass or fail recommendation for each core area exam, based on the exam grades received, transcripts, and personal comments solicited from faculty familiar with the candidate. The faculty at large will meet to review these recommendations and make a final determination as to whether or not the student has satisfied the requirement in each core area. The faculty at large will also be responsible for reviewing the situation of candidates who have failed to meet the comprehensive exam requirement within the allotted time.

### **Administration of the written portion of the Ph.D. Qualifying Exam**

Each member of the student's supervisory committee who holds a faculty rank in the University of Utah Computer Science Department must submit at least one question. Other committee members may each submit a question at their option.

The format of each question can be chosen by the committee member posing the question. Possibilities include (but are not limited to):

1. "take home" question, to be researched by the student using library, web, and other publicly available resources;
2. A "closed book sit down" examination, to be written during a fixed period without use of background materials;
3. An "open book sit down" examination, similar to (2), but permitting use of reference materials.

Note that in all cases, the examination will be written, not oral.

The entire exam should be completed in no more than seven days from initial question assignment to completed answers.

Grading should be completed within seven days after the student delivers his/her answers. Each committee member contributing a question will grade that question and provide a specific, written evaluation of the quality and correctness of the answer. Grades on individual questions should use the same grading scheme as used on the comprehensive exams: high pass (HP), pass (P), fail (F).

A grade of P- signifies the minimal acceptable performance expected from a Ph.D. student. An F+ indicates an answer that is partially correct, but not up to the standards we expect from our Ph.D. students.

The members contributing questions will each cast a Pass/Fail vote on the examination as a whole. An overall passing grade should be given to candidates who, through their answers, demonstrate that they are well prepared to conduct Ph.D. level research in their specialty area of computer science. The overall exam Pass/Fail grade will be determined by majority vote of those contributing questions. In the event of equal numbers of Pass and Fail votes, the deciding vote will be cast by the Director of Graduate Studies. The student will receive a standardized examiner evaluation form, recording the numeric Pass/Fail vote and a narrative evaluation, including dissenting judgments.

The examination questions, student answers, and examiner evaluation form will be retained in the student's departmental file.

A student who fails his/her first attempt may retake the exam once. No conditional pass grades will be given. However, the supervisory committee can at their option include fewer questions on repeated exams.

## **2.11 The M. Phil. Degree**

This degree requires the same qualifications for admission and scholarly achievement as the Ph.D, but does not require a dissertation and requires 54 semester hours of course work. All regulations covering the Ph.D. degree apply to the M.Phil. degree. This degree, like the Ph.D. degree, is a terminal degree; a student cannot be a candidate for both degrees in the same department. The Department considers applications for admission to the M.Phil. program only from students already matriculated in the Ph.D. program.

# 3

## Departmental Facilities

The Departmental Computing Facility is configured to support both instructional and research computing. The instructional facility, or General Computing Facility (GCF), supports both Unix and NT-based operating systems totaling 131 machines. Three major labs make up the bulk of the facility. The first consists of IBM Power PC systems running AIX; the second contains SGI machines ranging from Indy's to O2's; the third is an instructional NT lab housing Pentium II-based PC's. Students in the Department also have access to the College of Engineering Workstation Laboratory, which consists of five servers, 100 Sun Workstations, and twenty-five HP workstations. The machines are divided into two separate rooms and are used for undergraduate and graduate instruction.

The Research Computing Facility consists of a heterogeneous mix of machines from SGI, HP, IBM, Sun, and Intel totaling 238 machines, including some very specialized equipment such as a 96-node Origin 2000 with eight infinite reality pipes, an Evans and Sutherland Harmony system, and a 14-CPU Power Challenge. In addition to the systems, there are specialized labs for multi-source non-linear video editing, real-time signal processing, haptic (robotic arm-based force feedback), image analysis and various types of custom hardware design. Both facilities share a common network infrastructure that is based on an ATM fabric running at OC-12 (622 Mbps). The departmental network connects to the campus' OC-48 ATM mesh at OC-12 rates which, in turn, connect to the vBNS and Internet via dual OC-3 pipes. The base level of service to the desktop is Fast Ethernet (100 Mbps), with the capability to accept OC-3, OC-12, or Gigabit Ethernet connections where necessary. The department's network fabric will be fully switched by the Fall of 1999.

The major research computing facilities are composed of six laboratories: Computer-Aided Design and Graphics, Computer Systems Laboratory, Asynchronous Digital Systems and VLSI, Robotics and Vision, Scientific Computing and Imaging, and Information Retrieval and Natural Language Processing.

These research laboratories contain a wide array of specialized equipment, including

- an SGI Origin 2000 Reality Monster (96 processors, 8 IR heads);
- an SGI Power Onyx (14 processors, 2 RE2 heads);
- a variety of specialized equipment for image analysis, including a GRF-2 light striper and a DIGI-BOT II laser range scanner;
- two FDDI/Fast Ethernet clusters comprised of IBM RS 6000s, HP 700s, and SGI Indigo2s;
- a collection of graphics workstations from each workstation vendor;

- a multi-source video editing environment;
- robot arms;
- specialized D/A and A/D equipment for realtime signal processing.

The College of Engineering operates a research-scale integrated circuit (IC) fabrication facility that is used extensively by Computer Science. Equipment for testing and debugging both internally and externally fabricated circuits is housed in an integrated circuit testing facility that contains state-of-the-art HP, Tektronix and Micromanipulator automated IC testing equipment.

# 4

## Computer Science Courses

The number and title of each course is followed by the number of semester hours it carries, the semester(s) during which it is taught (F=fall, S=spring, U=summer), its prerequisites, its corequisites, and any courses with which it is cross-listed.

Where a course has both a 5000- and 6000-level number, the 5000-level version is intended for undergraduate and the 6000-level version for graduate students. The two versions of the class will meet together, but extra work will be expected of graduate students.

Current class schedules and registration information<sup>1</sup> are available on line.

### **1000 Engineering Computing** (3, FS) Coreq: CS 1010, MATH 1210.

An introduction to the use of widely available software tools to solve computational problems in science and engineering. Introductions to numerical and symbolic programming using Maple, and to procedural programming using C. Elementary numerical methods. Emphasis on the entire process of solving problems from science and engineering via computational means.

### **1010 Introduction to Unix** (0.5, FSU)

An introduction to the Unix workstations used in the College of Engineering CADE Lab. Topics include the X Windows system, Unix shell commands, file system issues, text editing with Emacs, accessing the World Wide Web with Netscape, and electronic mail. Self-paced course using online teaching aids.

### **1020 Introduction to Programming with C++** (3, U)

An introduction to essential programming concepts using C++. This course is more slowly paced and has much less depth than either CS 1000 (an introductory programming course designed for science and engineering majors) or CS 2010 (an introductory programming course designed for computer science and electrical engineering students).

### **1021 Introduction to Programming with Java** (3, FU)

An introduction to essential programming concepts using Java. This course is more slowly paced and has much less depth than either CS 1000 (an introductory programming course designed for science and engineering majors) or CS 2010 (an introductory programming course designed for computer science and electrical engineering students).

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<sup>1</sup><http://www.acs.utah.edu/student/index.htm>

**2010 Introduction to Computer Science I** (4, FS) Coreq: MATH 1210, CS 1010.

The first course required for students intending to major in computer science and computer engineering. Introduction to the engineering and mathematical skills required to effectively program computers, and to the range of issues confronted by computer scientists. Roles of procedural and data abstraction in decomposing programs into manageable pieces. Introduction to object-oriented programming. Extensive programming exercises that involve the application of elementary software engineering techniques.

**2020 Introduction to Computer Science II** (4, SU) Prereq: CS 2010.

The second course required for students intending to major in computer science and computer engineering. Introduction to the problem of engineering computational efficiency into programs. Classical algorithms (including sorting, searching, and graph traversal) and data structures (including stacks, queues, linked lists, trees, hash tables, and graphs). Analysis of program space and time requirements. Extensive programming exercises that require the application of elementary techniques from software engineering.

**2100 Discrete Structures** (2, F) Prereq: CS 2010.

Introduction to propositional logic, predicate logic, formal logical arguments, finite sets, functions, relations, inductive proofs, recurrence relations, graphs, and their applications to Computer Science.

**3100 Models of Computation** (3, S) Prereq: CS 2020, 2100.

Introduction to the mathematical underpinnings of computer science. Methods for describing and reasoning about hardware and software, including predicate logic, recursion, induction, and combinatorics. Models of sequential computation, including finite-state automata, push-down automata, and Turing machines.

**3200 Scientific Computation** (3, F) Prereq: CS 2020, MATH 2250.

Scientific computation relevant to computer science and engineering; floating-point arithmetic, systems of linear equations (direct and iterative techniques), nonlinear equations (univariate and multivariate), interpolation and differentiation (divided differences), integration (mechanical and Gaussian quadratures, optimal quadratures), approximation by spline functions (natural splines and B-splines, optimality of splines).

**3500 Software Practice** (4, F) Prereq: CS 2020.

Practical exposure to the process of creating large software systems, including requirements specifications, design, implementation, testing, and maintenance. Emphasis on software process, software tools (debuggers, profilers, source code repositories, test harnesses), software engineering techniques (time management, code and documentation standards, source code management, object-oriented analysis and design), and team development practice. Much of the work will be in groups and will involve modifying preexisting software systems.

**3510 Advanced Algorithms and Data Structures** (3, S) Prereq: CS 2100, CS 3500.

Study of algorithms, data structures, and complexity analysis beyond the introductory treatment from CS 2020. Balanced trees, heaps, hash tables, string matching, graph algorithms, external sorting and searching. Dynamic programming, exhaustive search. Space and time complexity, derivation and solution of recurrence relations, complexity hierarchies, reducibility, NP completeness.

**3520 Programming Language Concepts** (3, F) Prereq: CS 3510.

Ideas behind the design and implementation of programming languages. Syntactic description; scope and lifetime of variables; runtime stack organization; parsing and abstract syntax; semantic issues; type systems; programming paradigms; interpreters and compilers.

**3700 Fundamentals of Digital System Design** (4, S) Prereq: CS 2010, PHYCS 2220. Crosslisted with EE 3700.

Techniques for minimizing logic functions and designing common combinational circuits such as decoders, selectors, and adders. Synchronous and asynchronous sequential circuits, state diagrams, Mealy and Moore circuits, state minimization and assignment. Use of software tools for design, minimization, simulation, and schematic capture. Implementation with MSI, LSI, and field programmable gate arrays. Laboratory included.

**3710 Computer Design Laboratory** (3, F) Prereq: CS/EE 3700, CS/EE 3810. Crosslisted with EE 3710.

Student groups design, build, and test a programmable device such as a computer or calculator.

**3720 Analog & Digital Interfacing with Microprocessors & Microcontrollers** (4, S) Prereq: CS/EE 3700. Crosslisted with EE 3720.

Fundamentals of digital-to-analog (D-to-A) and analog-to-digital (A-to-D) circuits, relays, stepper motors, and digital switches. Interfacing digital and analog circuits to computers and micro-controllers. Laboratory included.

**3810 Computer Architecture** (4, F) Prereq: CS 2020. Crosslisted with EE 3810.

An in-depth study of computer architecture and design, from digital logic to operating systems, including topics such as pipelining, memory systems, parallel and serial communication, and interrupts. Performance measures and compilation issues. Computer architectures including RISC, CISC, stack, and parallel.

**4500 Software Engineering Laboratory** (3, S) Prereq: CS 3510 or CS 4510, senior standing in CS.

Development of significant software systems by small student groups, with emphasis on applying sound, disciplined software engineering practice.

**4510 Software Engineering** (3, F) Prereq: Senior standing in CS.

Fundamentals of software engineering, including requirements, specifications, design, implementation, testing, and maintenance. Emphasis includes: software process, software engineering techniques (code and documentation standards, source code management, object-oriented analysis and design), and team development practice. (This course will be offered during Fall 1999 and will then be discontinued. It is intended for students who completed the CS 354-355-356 sequence under quarters; CS 3500 and CS 4510 may not both be taken for credit.)

**4710 Computer Engineering Senior Project** (3, F) Prereq: CS/EE 3710, CS/EE 3720. Crosslisted with EE 4710.

Students design microcomputer system that includes RAM, EPROM, and I/O devices. Capstone project for computer engineering majors. Formal written reports, one or more oral presentations. (Not offered Fall 1999.)

**4950 Independent Study** (Arr.)

**4960 Special Topics** (Arr.)

The following special topics courses are currently scheduled for the 1999-2000 academic year. Contact the faculty member in charge for details.

- **CS 4960 Simulation** (3, F). Prof. Henderson.

**4970 Bachelor's Thesis** (3) Prereq: Permission of instructor, senior standing in CS.

Only students who have previously worked with a faculty member in a research group may register for Bachelor's Thesis credit, and then only with the permission of the faculty member. An undergraduate thesis is a publication-quality description of work done in previous semesters. At a minimum a thesis must be published as a technical report; ideally, it should be submitted to a conference or journal. A Bachelor's Thesis is intended as an alternative to the senior Software Engineering Laboratory for students who are headed for graduate school.

**4999 Honors Thesis/Project** (3) Prereq: Restricted to students in the Honors Program working on their Honors degree.

**5010 Software Practice** (4, F) Prereq: CS 2020 and permission of instructor.

This course is for graduate students from departments other than Computer Science. Practical exposure to the process of creating large software systems, including requirements specifications, design, implementation, testing, and maintenance. Emphasis on software process, software tools (debuggers, profilers, source code repositories, test harnesses), software engineering techniques (time management, code and documentation standards, source code management, object-oriented analysis and design), and team development practice. Much of the work will be in groups and will involve modifying preexisting software systems.

**5020 Advanced Algorithms and Data Structures** (3, S) Prereq: CS 5010 and permission of instructor.

This course is for graduate students from departments other than Computer Science. Study of algorithms, data structures, and complexity analysis beyond the introductory treatment from CS 2020. Balanced trees, heaps, hash tables, string matching, graph algorithms, external sorting and searching. Dynamic programming, exhaustive search. Space and time complexity, derivation and solution of recurrence relations, complexity hierarchies, reducibility, NP completeness.

**5100/6100 Foundations of Computer Science** (3, F) Prereq: CS 3100, CS 3510.

Advanced examination of fundamental ideas behind algorithms, complexity analysis, mathematical logics, elementary computability, and concurrency formalisms.

**5210/6210 Advanced Scientific Computing I** (3) Prereq: CS 3200, CS 3510, MATH 3160.

An introduction to existing classical and modern numerical methods and their algorithmic development and efficient implementation. Topics include: numerical linear algebra, interpolation, approximation methods and parallel computation methods for nonlinear equations, ordinary differential equations, and partial differential equations. (Offered every third semester; next scheduled offering Fall 2000.)

**5300/6300 Artificial Intelligence** (3, F) Prereq: CS 3510.

Introduction to field of artificial intelligence, including heuristic programming, problem-solving, search, theorem proving, question answering, machine learning, pattern recognition, game playing, robotics, computer vision.

**5310/6310 Robotics** (3, F) Prereq: CS 1000, MATH 2250, PHYCS 2220. Crosslisted with ME 5220/6220.

The mechanics of robots, comprising kinematics, dynamics, and trajectories. Planar, spherical, and spatial transformations and displacements. Representing orientation: Euler angles, angle-axis, and quaternions. Velocity and acceleration: the Jacobian and screw theory. Inverse kinematics: solvability and singularities. Trajectory planning: joint interpolation and Cartesian trajectories. Statics of serial chain mechanisms. Inertial parameters, Newton-Euler equations, D'Alembert's principle. Recursive forward and inverse dynamics.

**5320/6320 Computer Vision** (3, S) Prereq: CS 3510, MATH 2210, MATH 2270.

Basic pattern-recognition and image-analysis techniques, low-level representation, intrinsic images, "shape from" methods, segmentation, texture and motion analysis, and representation of 2-D and 3-D shape.

**5340/6340 Natural Language Processing** (3, S) Prereq: CS 3510; CS 5300/6300 recommended.

Computational models and methods for understanding written text. Introduction to syntactic analysis, semantic analysis, discourse analysis, knowledge structures, and memory organization. A variety of approaches are covered, including conceptual dependency theory, connectionist methods, and statistical techniques. Applications include story understanding, fact extraction, and information retrieval.

**5350/6350 Machine Learning** (3, F) Prereq: CS 3510; CS 5300/6300 recommended.

Techniques for developing computer systems that can acquire new knowledge automatically or adapt their behavior over time. Topics include concept learning, decision trees, evaluation functions, clustering methods, explanation-based learning, language learning, cognitive learning architectures, connectionist methods, reinforcement learning, genetic algorithms, hybrid methods, and discovery. (Offered alternate years; next scheduled offering Fall 1999.)

**5460 Operating Systems** (3, F) Prereq: CS 3510, CS/EE 3810.

Characteristics, objectives, and issues concerning computer operating systems. Hardware/software interactions, process management, memory management, protection, synchronization, resource allocation, file systems, security, and distributed systems. Extensive systems programming.

**5470 Compiler Principles and Techniques** (3, S) Prereq: CS 3510, CS/EE 3810, CS 3100.

Lexical analysis, top-down and bottom-up parsing, symbol tables, internal forms and intermediate languages, runtime environments, code generation, code optimization, semantic specifications, error detection and recovery. Use of software tools for lexical analysis and parsing.

**5480/6480 Data Communications and Networks** (3, F) Prereq: CS 3510, CS/EE 3810.

A comprehensive study of the principles and practices of data communication and networks. Topics include: transmission media, data encoding, local and wide area networking architectures, internetwork and transport protocols (e.g., IPv4, IPv6, TCP, UDP, RPC, SMTP), networking infrastructure (e.g., routers, nameservers, gateways), network management, distributed applications, network security, and electronic commerce. Principles are put into practice via a number of programming projects.

**5530/6530 Database Systems** (3, F) Prereq: CS 3510.

Representing information about real world enterprises using important data models including the entity-relationship, relational and object-oriented approaches. Database design criteria, including normalization and integrity constraints. Implementation techniques using commercial database management system software. Selected advanced topics such as distributed, temporal, active, and multi-media databases.

**5540/6540 Human/Computer Interaction** (3, F) Prereq: CS 3510.

Fundamentals of input/output devices, user interfaces, and human factors in the context of designing interactive applications. (Not offered Fall 1999.)

**5600/6600 Computer Graphics I** (3, F) Prereq: CS 3510, MATH 2250.

Basic display techniques, display devices, vector generation, display processors. Homogeneous coordinates, transformations, and clipping in 2-D. Graphics systems, interactive graphics. Introduction to raster graphics. Some elements of photography as related to computer graphics.

**5610/6610 Computer Graphics II** (3, S) Prereq: CS 5600/6600.

Representations of 3-D objects, polygons, 3-D visualization techniques, hidden-line and hidden- surface removal, polygon clipping, continuous-tone pictures, color displays, lighting models, the aliasing problem. Some fundamentals of photographing computer-generated gray-scale images.

**5630/6630 Scientific Visualization** (3) Prereq: CS 3510; CS 3200 or CS 5210 or MATH 5600.

Introduction to the techniques and tools needed for the visual display of data. Students will explore many aspects of visualization, using a "from concepts to results" format. The course begins with an overview of the important issues involved in visualization, continues through an overview of graphics tools relating to visualization, and ends with instruction in the utilization and customization of a variety of scientific visualization software packages. (Offered every third semester; next scheduled offering Spring 2000.)

**5710/6710 Advanced Integrated Circuit Design I** (3, F) Prereq: CS/EE 3700. Crosslisted with EE 5710/6710.

Introduction to basic concepts of the design of CMOS integrated circuits for students with a wide range of backgrounds. Static and dynamic properties of CMOS circuits, composite layout of CMOS circuits, and modeling of transistors for use in SPICE simulations. Commonly encountered CMOS circuits. Introduction to CMOS analog/digital circuits. Students complete design, composite layout, and digitization of a simple integrated circuit using computer-aided design tools.

**5720/6720 Advanced Integrated Circuit Design II** (3, S) Prereq: CS/EE 5710/6710, EE 2100. Crosslisted with EE 5720/6720.

Design of mixed signal (analog/digital) CMOS integrated circuits. Fundamental building blocks for analog circuits, including the basic principles of opamp, current mirror and comparator design. Basics of discrete-time signals and filters. Implementation of switched capacitor circuits and discussions of various implementations of D/A and A/D converters, oversampled converters and phase locked loops.

**5740/6740 Computer-Aided Design of Digital Circuits** (3, F) Prereq: CS/EE 3700, CS 3510. Crosslisted with EE 5740/6740.

Introduction to theory algorithms used for computer-aided synthesis of digital integrated circuits. Topics include algorithms and representations for Boolean optimization, hardware modeling, combination logic optimization, sequential logic optimization and technology mapping. (Offered alternate years; next scheduled offering Fall 2000.)

**5750/6750 Synthesis and Verification of Asynchronous VLSI Systems** (3, S) Prereq: CS/EE 3700, CS 3510. Crosslisted with EE 5750/6750.

Introduction to systematic methods for the design of asynchronous VLSI systems from high-level specifications to efficient, reliable circuit implementations. Topics include specification, controller synthesis, optimization using timing information, technology mapping, data path design, and verification. (Offered alternate years; next scheduled offering Spring 2000.)

**5810/6810 Advanced Computer Architecture** (3, F) Prereq: CS/EE 3700, CS/EE 3810. Crosslisted with EE 5810/6810.

Principles of modern high performance computer and micro architecture: static vs. dynamic issues, pipelining, control and data hazards, branch prediction and correlation, cache structure and policies, cost-performance and physical complexity analyses.

**5830/6830 VLSI Architecture** (3, S) Prereq: CS/EE 3710, CS/EE 3810.

Project-based study of a variety of topics related to VLSI systems. Use of field programmable gate arrays to design, implement, and test a VLSI project. (Offered alternate years; next scheduled offering Spring 2001.)

**5940 Seminar** (1-3)

Current topics in computer science. May be repeated for credit.

**5950 Independent Study** (Arr.)

**5960–5964 Special Topics** (Arr.)

The following special topics courses are currently scheduled for the 1999–2000 academic year. Contact the faculty member in charge for details.

- **CS 5960/6960 Advanced Compilers** (3, F). Prof. Hsieh.
- **CS 5962 Electronic Commerce** (2, F). Prof. Hollaar.
- **CS 5963 Advanced Manufacturing** (2,F). Prof. Drake.

**6010 Writing Research Proposals** (2, S)

Fundamental aspects of writing computer science research proposals, including thesis, dissertation, and grant proposals. Form, style, substance, and marketing of effective proposals will be considered. Emphasis is placed on developing and presenting clear and compelling ideas. Substantial writing and class presentations is required of all participants. (This is a half-semester course.)

**6020 Writing Research Papers** (3, S)

This course involves the presentation of research that has been published (or accepted for publication) to the department. The student must give a one-hour presentation of the research in the paper to the entire department (preferably in a colloquium slot). It must be taken as soon as possible following acceptance of the paper for publication. May only be taken once for credit, and must be taken in the first three years of graduate school.

**6110 Formal Methods for System Design** (3, S) Prereq: CS 5100/6100 and CS 6520.

Study of methods for formally specifying and verifying computing systems. Specific techniques include explicit state enumeration, implicit state enumeration, automated decision procedures for first-order logic, and automated theorem proving. Examples selected from the areas of superscalar CPU design, parallel processor memory models, and synchronization and coordination protocols. (Offered alternate years; next scheduled offering Fall 1999.)

**6220 Advanced Scientific Computing II** (3) Prereq: CS 5210/6210 or MATH 5600.

A study of the numerical solution of two and three dimensional partial differential equations that arise in science and engineering problems. Topics include: finite difference methods, finite element methods, boundary element methods, multigrid methods, mesh generation, storage optimization methods, and adaptive methods. (Offered every third semester; next scheduled offering Fall 1999.)

**6360 Virtual Reality** (3, S) Prereq: CS 5310/6310.

Human interfaces: visual, auditory, haptic, and locomotory displays; position tracking and mapping. Computer hardware and software for the generation of virtual environments. Networking and communications. Telerobotics: remote manipulators and vehicles, low-level control, supervisory control, and real-time architectures. Applications: manufacturing, medicine, hazardous environments, and training. (Offered alternate years; next scheduled offering Spring 2001.)

**6520 Programming Languages and Semantics** (3, S) Prereq: CS 3520.

Examination of the formal and pragmatic ideas behind programming language design. Imperative, functional, logic, object-oriented, and multi-paradigm languages. Lambda calculus, fixpoints, type systems, and predicate logic. Denotational semantics and models of concurrency.

**6620 Image Synthesis** (3, S) Prereq: CS 5610/6610, CS 6670 MATH 5010.

Using camera and sensor simulation along with physical simulation to generate realistic synthetic images. (Offered alternate years; next scheduled offering Spring 2000.)

**6670 Computer-Aided Geometric Design I** (3, F) Prereq: MATH 2210, MATH 2250, CS 3510; Coreq: CS 5600/6600.

**6680 Computer-Aided Geometric Design II** (3, S) Prereq: CS 6670.

Introduction to current concepts and issues in CAGD systems with emphasis on free-form surface design; mathematics of free-form curve and surface representations, including Coons patches, Bezier method, B-splines, triangular interpolants, and their geometric consequences; classical surface geometry; local and global design tradeoffs and explicit and parametric tradeoffs; subdivision and refinement as techniques in modeling; current production capabilities compared to advanced research. Laboratory experiments with current CAD systems. (Offered alternate years; next scheduled offering 2000-2001 academic year.)

**6770 Advanced Digital VLSI Systems Design** (3, S) Prereq: CS/EE 5710/6710. Crosslisted with EE 6770.

Full custom, high speed, high performance CMOS circuit design issues, methodologies, and techniques. Failure modes, modeling techniques, testing, clock skew analysis, clock distribution, power analysis, power line distribution, electrical rules checking, megacell design flow, and other important design issues.

**6820 Parallel Computer Architecture** (3, S) Prereq: CS/EE 5810/6810. Crosslisted with EE 6820.

Architecture, design, and analysis of parallel computer systems: vector processing, data vs. control concurrency, shared memory, message passing, communication fabrics, case studies of current high performance parallel systems. (Offered alternate years; next scheduled offering Spring 2001.)

**6930–6944 Seminar** (1-3)

Current topics in Computer Science. May be repeated for credit.

**6950 Independent Study** (Arr.)

**6960–6964 Special Topics** (Arr.)

The following special topics courses are currently scheduled for the 1999–2000 academic year. Contact the faculty member in charge for details.

- **CS 6961 Advanced Graphical Rendering** (3, F). Prof. Shirley.
- **CS 6964 Perception and Graphics** (3,S). Prof. Thompson.

**6970 Masters Thesis Research** (Arr.)

**6980 Faculty Consultation Masters** (Arr.)

**7120 Information-Based Complexity** (3, S) Prereq: CS 3200, MATH 2270, MATH 3210.

Analysis of optimal computational methods for continuous problems. Introduction to the general worst case theory of optimal algorithms, linear problems, and spline algorithms as well as selected nonlinear problems. Examples include optimal integration, approximation, nonlinear zero finding, and fixed points. (Offered alternate years; next scheduled offering Spring 2000.)

**7240 Sinc Methods** (3) Prereq: CS 5210/6210 or MATH 5600 or MATH 5610.

Sinc methods for solving difficult computational problems, such as partial differential and integral equation problems, that arise in science and engineering research. Emphasis on parallel computation. Applications vary, depending on participants in the class. Students are given projects—whenever possible in their areas of research—that lead to publishable research articles. (Offered alternate years; next scheduled offering Spring 2000.)

**7310 Advanced Robotics** (3, S) Prereq: CS/ME 5310/6310 5220/6220. Crosslisted with ME 7230.

This course covers the kinematics, dynamics, and control of robotic manipulators. Projects controlling robots will be an integral part of the course. (Offered alternate years; next scheduled offering Spring 2000.)

**7460 Advanced Operating Systems** (3) Prereq: CS 5460/6460, CS 5480/6480.

Practical distributed operating systems concepts from basics through the state of the art. Topics include interprocess communication, client-server systems, distributed shared memory, distributed file systems, distributed databases, portable computing, software fault tolerance, and wide-area (e.g. web) applications. Work includes individual oral presentations, a group project, and a written research report. (Offered alternate years; next scheduled offering Spring 2000.)

**7970 PhD Dissertation Research** (Arr.)**7980 Faculty Consultation PhD** (Arr.)**7990 Continuing Registration: Ph.D.** (Arr.)

# 5

## CS Faculty And Their Research Interests

### **Erik Brunvand**

Associate Professor of Computer Science  
Ph.D., Carnegie Mellon University, 1991

Professor Brunvand<sup>1</sup> joined the Department of Computer Science in 1990. He has interests in computer architecture and VLSI systems in general, and self-timed and asynchronous systems in particular. One aspect of his research involves compiling concurrent communicating programs into asynchronous VLSI circuits. The current system allows programs written in a subset of Occam, a concurrent message-passing programming language based on CSP, to be automatically compiled into a set of self-timed circuit modules suitable for manufacture as an integrated circuit. He is also interested in investigating the effects of asynchrony on computer systems architecture at a higher level. To explore these ideas he is building a series of prototype asynchronous computer systems out of FPGA and custom VLSI chips.

- Ganesh Gopalakrishnan, Prabhakar Kudva, and Erik Brunvand, "Peephole Optimization of Asynchronous Macro-module Networks," IEEE Transactions on VLSI Systems, Vol. 7, No 1, March 1999.
- William Richardson and Erik Brunvand, "Fred: A Decoupled Self-Timed Computer Architecture with Precise Exceptions," IEE Proceedings on Computers and Digital Techniques, Special issue on Asynchronous Processors. Vol. 143, No. 5, September 1996.
- Erik Brunvand, Steven Nowick, and Kenneth Yun, "Modern Asynchronous Circuit Design," TAU-99, Monterey, CA, March 1999.
- J. Carter, W. Hsieh, L. Stoller, M. Swanson, L. Zhang, E. Brunvand, A. Davis, C.-C. Kuo, R. Kuramkote, M. Parker, L. Schaelicke, and T. Tateyama, "Impulse: Building a Smarter Memory Controller," The Proceedings of the Fifth International Symposium on High Performance Computer Architecture, Jan 1999
- Ajay Khoche and Erik Brunvand, "Critical Hazard-Free Test Generation for Asynchronous Circuits," VLSI Test Symposium (VTS'97).
- William Richardson and Erik Brunvand, "Precise Exception Handling for a Self-Timed Processor," in IEEE International Conference on Computer Design (ICCD95) *Winner of best paper award in Design & Test track at ICCD95.*

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<sup>1</sup><http://www.cs.utah.edu/~elb/>

**John Carter**

Assistant Professor of Computer Science  
Ph.D., Rice University, 1992

Professor Carter<sup>2</sup> joined the Department of Computer Science in January 1993. His research interests include multiprocessor computer architecture, operating systems, distributed computing, and computer networks. Of particular interest are scalable shared memory architecture designs, both hardware and software. Dr. Carter is co-leading two research projects: the Impulse Adaptable Memory Systems project and the Khazana project. The goal of the Impulse project is to attack the primary problem limiting performance in future computer systems—the inability of conventional memory systems to supply data fast enough to avoid processing stalls—by developing a main memory controller and associated software that allows applications to dynamically change the way that the processor’s memory hierarchy is managed. Khazana makes it easier for programmers to develop sophisticated distributed applications by addressing the shared state management problem faces by most such applications. Khazana exports the abstraction of a distributed secure persistent globally shared store that applications can use to store their shared state. It is responsible for performing many of the common operations needed by distributed applications, including replication, consistency management, and fault recovery.

- J.B. Carter, W.C. Hsieh, et al. Impulse: Building a Smarter Memory Controller. In the *Proceedings of the Fifth International Symposium on High Performance Computer Architecture*, pp. 70-79, January 1999.
- C.-C. Kuo, J.B. Carter, R. Kuramkote, and M. Swanson. AS-COMA: An Adaptive Hybrid Shared Memory Architecture. In the *Proceedings of the 1998 International Conference on Parallel Processing (ICPP’98)*, August 1998.
- M. Swanson, L.B. Stoller, and J.B. Carter. Increasing TLB Reach Using Superpages Backed by Shadow Memory. In the *Proceedings of the 25th Annual International Symposium on Computer Architecture*, June 1998, pp. 204-213.
- J.B. Carter, A. Ranganathan, and S. Susarla. Khazana: An Infrastructure for Building Distributed Services. In the *Proceedings of the 18th Annual International Conference on Distributed Computing Systems*, pp. 562-571, May 1998.
- J.B. Carter, J.K. Bennett, and W. Zwaenepoel. Techniques for reducing consistency-related communication in distributed shared memory systems. *ACM Transactions on Computer Systems*, August 1995.
- J.B. Carter, J.K. Bennett, and W. Zwaenepoel. Implementation and performance of Munin. In *Proceedings of the 13th ACM Symposium on Operating Systems Principles*, pages 152–164, October 1991.

**Elaine Cohen**

Professor of Computer Science  
Adjunct Associate Professor of Mathematics  
Ph.D., Syracuse University, 1974

Professor Cohen<sup>3</sup> has held a faculty position in the Department of Computer Science since 1974. Currently she is co-head of the department’s Computer-Aided Geometric Design Group. Present research is centered around representational and algorithmic problems associated with geometric modeling, graphics, scientific visualization physically based modeling, process planning, CAD/CAM and CAE. Dr. Cohen received a B.A. in Mathematics from Vassar College in 1968 and M.S. and Ph.D. degrees in mathematics from Syracuse University in 1970 and 1974, respectively.

- T. V. Thompson II, D. E. Johnson, and E. Cohen., ”Direct Haptic Rendering Of Sculptured Models,” in Proc. Symposium on Interactive 3D Graphics, (Providence, RI), pp. 167-176, ACM, April 1997.
- G. Elber and E. Cohen, ”Adaptive Iso-Curves Based Rendering for Free Form Surfaces,” *Transactions on Graphics*, 1996, v. 15, n. 3, pp.249 - 263.
- E. Driskill and E. Cohen, ”Interactive Design, Analysis, and Illustration of Assemblies,” in Proc. Symposium on Interactive 3D Graphics, pp. 27-32, ACM, April 1995.

<sup>2</sup><http://www.cs.utah.edu/~retrac/>

<sup>3</sup><http://www.cs.utah.edu/~cohen/>

- G. Elber and E. Cohen, "Arbitrarily Precise Computation of Gauss Maps and Visibility Sets," in Proc. Solid Modeling 95, (Salt Lake City, UT), Solid Modeling, May 1995.
- R. Riesenfeld, E. Cohen, S. Drake, and L. Gursoz, "Modeling Issues in Solid Freeform Fabrication", in Proc. NSF Solid Freeform Fabrication Workshop, 1995.

#### **Al Davis**

Professor of Computer Science  
Ph.D., University of Utah, 1972

Professor Davis<sup>4</sup> joined the Department in 1993. His research interests involve high performance computer architectures and digital system design methodologies. More specifically he is interested in parallel processor architectures, high performance uniprocessor I/O architecture, VLSI, VLSI CAD, high performance communication, and asynchronous circuits. Prior to his joining the faculty in the fall of 1993, he spent the previous 12 years as a research scientist working on the design and implementation of parallel processing systems at Schlumberger Palo Alto Research and subsequently at Hewlett-Packard Laboratories. Recent accomplishments include 1) the development of an automatic asynchronous circuit synthesis system called STETSON; 2) the design and implementation of an asynchronous scalable parallel communication fabric VLSI component called FEDEX which is capable of supporting 500 MB/sec sustained bandwidth on each of its 7 ports; and 3) the development of an extensible and scalable parallel processing system called MAYFLY and 4) the development of a very low latency message passing protocols called Direct Deposit. He is currently involved in the DARPA sponsored Impulse project (Prof. John Carter is the PI) which is developing an adaptive memory controller that is capable of dynamically organizing cache lines to suit the applications needs. During the 1999-2000 academic year, Professor Davis will be on sabbatical at Intel in Austin, Texas where he is leading the I/O and memory architecture efforts for the IA32 processor which is expected to be in production in 2003.

- J. Carter, W. Hsieh, L. Stoller, M. Swanson, L. Zhang, E. Brunvand, A. Davis, C.-C. Kuo, R. Kuramkote, M. Parker, L. Schaelicke, and T. Tateyama. Impulse: Building a Smarter Memory Controller. Proceedings of the Fifth International Symposium on High Performance Computer Architecture, pp. 70-79, January 1999.
- L. Schaelicke and A. Davis. Improving I/O Performance with a Conditional Store Buffer. Proceedings of the 31st Annual ACM/IEEE International Symposium on Microarchitecture, pp. 160-169, November 1998.
- A. Davis, M. Swanson, M. Parker. Efficient Communication Mechanisms for Cluster Based Parallel Computing. Springer-Verlag Lecture Notes in Computer Science #1199. Feb. 1997, pp. 1-15.
- A. Davis. *Asynchronous Digital Circuit Design*. Chapter 3: 'Synthesizing Asynchronous Circuits: Practice and Experience'. Springer-Verlag Workshops in Computing series, April 1995, pp. 104 - 151.
- A. Davis. R2 - A Damped Adaptive Router Design. Parallel Computer Routing and Communication. Springer-Verlag Lecture Notes in Computer Science #853. May 1994, pp. 295-309.
- A. Davis. Mayfly: A General-Purpose, Scalable, Parallel Processing Architecture. Lisp and Symbolic Computation, Volume 5, Numbers 1/2, May 1992, pp. 7-47.

#### **Matthew Flatt**

Research Assistant Professor of Computer Science  
Ph.D., Rice University, 1999

Professor Flatt's<sup>5</sup> research interests cover all practical and theoretical aspects of programming languages and environments. As part of the Programming Languages Team (PLT) at Rice, he was one of the principal architects of the DrScheme programming environment, and he helped to develop the TeachScheme! introductory curriculum. His current research topics include module languages to enable software components, object-oriented languages to support both classes and mixins, and high-level operating systems to promote reliable and extensible applications.

<sup>4</sup><http://www.cs.utah.edu/~ald/>

<sup>5</sup><http://www.cs.utah.edu/~mflatt/>

- M. Flatt, R. B. Findler, S. Krishnamurthi, and M. Felleisen. Programming Languages as Operating Systems (*or, Revenge of the Son of the Lisp Machine*). In *Proc. ACM International Conference on Functional Programming*, September 1999.
- M. Flatt and M. Felleisen. Cool Modules for HOT Languages. In *Proc. ACM Sigplan Conference on Programming Language Design and Implementation*, June 1998.
- M. Flatt, S. Krishnamurthi, and M. Felleisen. Classes and Mixins. In *Proc. ACM Symposium on Principles of Programming Languages*, January 1998. Expanded version as a chapter in *Formal Syntax and Semantics of Java*, LNCS 1523, Springer.
- R. B. Findler and M. Flatt. Modular Object-Oriented Programming with Units and Mixins. In *Proc. ACM International Conference on Functional Programming*, September 1998.
- R. B. Findler, C. Flanagan, M. Flatt, S. Krishnamurthi and M. Felleisen. *DrScheme* A Pedagogic Programming Environment for Scheme. In *Proc. International Symposium on Programming Languages: Implementations, Logics, and Programs*, September 1997.

### Ganesh C. Gopalakrishnan

Associate Professor of Computer Science

Ph.D., State University of New York at Stony Brook, 1986

Professor Gopalakrishnan's<sup>6</sup> research is in formal methods and self-timing. Formal methods help build reliable computing systems through powerful verification methods that unearth insidious bugs, help improve the quality of the documentation, and offer a reliable starting point for product upgrades. The focus of our Utah Verifier group has been on verification, and our recent results include a systematic method for decomposing proofs of pipelined processors, formalization and analysis of transaction ordering problems in I/O systems, efficient partial order reduction algorithms incorporated into new model-checkers, and a model-checking method for verifying shared memory systems. Self-timing contributes to the robustness of operation of very large-scale integrated (VLSI) digital systems through increased reliance on explicit completion sensing as well as control orchestration through handshake signals. Our focus has been to develop a tool-path called ACK (asynchronous circuit compiler) to synthesize self-timed systems consisting of data-paths and controllers, and also develop efficient programmable self-timed controllers based on microprogramming. Professor Gopalakrishnan was a general co-Chair of the Internal Symposium on Formal Methods in Computer-Aided Design (FMCAD) in November 1998 and of the International Symposium on Advanced Research in Asynchronous Circuits and Systems (Async) in November 1994. He is a member of IFIP WG 10.5 that coordinates as well as sponsors conferences in design automation and verification. Generally, he teaches applied theory classes as well as special topics classes in design automation of hardware.

- Hans M. Jacobson and Ganesh Gopalakrishnan, "Application-Specific Programmable Control for High-Performance Asynchronous Circuits", Invited Paper, Proceedings of IEEE, Vol. 87, No. 2, Feb 1999, pp. 319-331.
- Ratan Nalumasu and Ganesh Gopalakrishnan, "Deriving Efficient Cache Coherence Protocols Through Refinement", *Formal Methods in System Design* (Kluwer). Special issue on Unity. To appear.
- Ravi Hosabettu, Mandayam Srivas, and Ganesh Gopalakrishnan, "Proof of Correctness of a Processor with Reorder Buffer using the Completion Functions Approach", Computer Aided Verification, 11th International Conference, CAV'99, Trento, Italy, July 7-10, 1999. pp. 47-59.
- Ratan Nalumasu, Rajnish Ghughal, Abdel Mokkedem, and Ganesh Gopalakrishnan, "The 'Test Model-checking' Approach to the Verification of Formal Memory Models of Multiprocessors", Computer Aided Verification, 10th International Conference, CAV'98, Vancouver, B.C., Canada, June/July 1998, Proceedings. Alan J. Hu and Moshe Y. Vardi (editors). Springer-Verlag LNCS 1427. pp. 464-476.
- Ganesh Gopalakrishnan and Phillip Windley, editors. Formal Methods in Computer-Aided Design. Proceedings of the 2nd International Conference, FMCAD '98. Lecture Notes in Computer Science 1522, Springer-Verlag, 1998, 538 pages. ISSN 0302-9743.

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<sup>6</sup><http://www.cs.utah.edu/~ganesh/>

**David H. Hanscom**

Clinical Professor of Computer Science  
Ph.D., Case Western Reserve University, 1970

Professor Hanscom's<sup>7</sup> background is in the field of communications processor design at Sperry Univac, where he worked from 1970 to 1982. Since then he has been responsible for administering the undergraduate Computer Science program at the University of Utah. In that capacity he teaches core computer science classes, serves as faculty advisor for individual students and for the Student Chapter of the Association for Computing Machinery, participates in student recruiting activities, and serves as director of the High School Computing Institute. His interests are in the areas of undergraduate education, computer architecture, and data communications.

- Hanscom, D.H., *Instructor's Manual to Accompany Structures and Abstractions, An Introduction to Computer Science with Pascal*, by William I. Salmon, Richard D. Irwin, 1991, 393 pages.

**Charles Hansen**

Associate Professor of Computer Science  
Ph.D., University of Utah, 1987

Professor Hansen<sup>8</sup> joined the Department in 1998. His research interests span scientific visualization, computer graphics, and high performance computing. Scientific visualization of large scale problems is of key interest and recent work involves taking into consideration time-varying data and exploiting this for speeding up the visualization process. Other methods for visualizing large amounts of data are multiresolution models and view dependent algorithms. The interest in computer graphics is driven from the scientific visualization perspective but includes parallel algorithms for speeding up global illumination.

- S. Parker, M. Parker, Y. Livnat, P-P. Sloan, C. Hansen, and P. Shirley. Interactive Ray Tracing for Volume Visualization. In *IEEE Transactions on Visualization and Computer Graphics*, 1999.
- T. Udeshi and C. Hansen. Towards interactive photorealistic rendering of indoor scenes: A hybrid approach. In *Eurographics Rendering Workshop 1999*.
- P-P. Sloan and C. Hansen. Parallel Lumigraph Reconstruction. In *Parallel Visualization and Graphics Symposium 1999*.
- P. Sutton and C. Hansen. Isosurface Extraction in Time-varying Fields Using a Temporal Branch-on-Need Tree (T-BON). In *Visualization '99*, 1999.
- Y. Livnat and C. Hansen. View Dependent Isosurface Extraction. In *Visualization '98*, 1998.

**Thomas C. Henderson**

Professor of Computer Science  
Ph.D., University of Texas, 1979

Professor Henderson's<sup>9</sup> professional interests include autonomous agents, multisensor systems, and simulation. Major areas of current research are robot behavior specification, simulation, and multisensor integration. Prior to his arrival at Utah, he was a visiting professor at the Institut National de Recherche en Informatique et en Automatique (INRIA), France, and a Research Associate at the Institut fuer Nachrichtentechnik, Deutsche Forschungs und Versuchsanstalt fuer Luft und Raumfahrt (DFVLR), West Germany.

- "Smart Sensor Snow," Thomas C. Henderson, Mohamed Dekhil, Scott Morris, Yu Chen and William B. Thompson, IEEE Conference on Intelligent Robots and Intelligent Systems, Victoria, CA, 13-16 Oct, 1998.

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<sup>7</sup><http://www.cs.utah.edu/~hanscom/>

<sup>8</sup><http://www.cs.utah.edu/~hansen/>

<sup>9</sup><http://www.cs.utah.edu/~tch/>

- “Instrumented Sensor System Architecture,” Mohamed Dekhil and Thomas C. Henderson, *International Journal of Robotics Research*, Vol. 17, No. 4, April, pp. 402–417, 1998.
- “Flat Surface Reconstruction Using Sonar,” Thomas C. Henderson, Mohamed Dekhil, Beat Brüderlin, Larry Schenkat and Larkin Veigel, *International Journal of Robotics Research*, Vol 17, No. 5, May, pp. 504-511, 1998.
- “Evolutionary Teleomorphology,” *Journal of Robotics and Autonomous Systems*, Vol. 19, No. 1, November 1996, pp. 23–32. (Thomas C. Henderson and Alexei A. Efros).
- Henderson T. C. with S. Susswein, J. Zachary, C. Hansen, P. Hinker, and G. Marsden, “Parallel Path Consistency,” *International Journal of Parallel Programming*, Vol. 20, No. 6, 1992.
- Henderson, T.C. with Chuck Hansen, *CAGD-Based Computer Vision*, IEEE Trans. on Pattern Analysis and Machine Intelligence, Vol. 11, No. 11, pp. 1181-1193, 1989.
- Henderson, T.C. with Amar Mitiche, Eliot Weitz, Chuck Hansen, *Multisensor Knowledge Systems: Interpreting 3-D Structure*, International Journal of Robotics Research, Special Issue on Multisensor Integration, Vol. 7, No. 6, pp. 114-137, 1988.

#### **Lee A. Hollaar**

Professor of Computer Science

Ph.D., University of Illinois at Urbana-Champaign, 1975

Professor Hollaar’s<sup>10</sup> primary interest is in legal issues regarding computers, particularly the intellectual property protection of software and information. As a Fellow with the Committee on the Judiciary, he has advised the United States Senate on computer-related issues such as encryption, copyright and patent, and regulation of the internet. He was one of the drafters of the Utah Digital Signature Act, the first law in the world to legally recognize digital signatures, and is active in the implementation of the required infrastructure. He directed the Utah Retrieval System Architecture (URSA) project, which developed hardware and software systems to support large information retrieval systems, including a special-purpose VLSI processor for the rapid searching of text and one of the first workstation-based client-server distributed systems for information retrieval. He was also the University’s director of campus networking, and continues to work in communications networks and distributed systems.

- Hollaar, L.A., *Now That the CDA’s History, Let’s Plan Anew*, The National Law Journal, July 14, 1997.
- Hollaar, L.A., *Justice Douglas Was Right: The Need for Congressional Action on Software Patents*, AIPLA Quarterly Journal, Winter 1996.
- Hollaar, L.A., *Method for Error Recovery in a Digital Data Communications System*, United States Patent 5,396,613, March 7, 1995.
- Hollaar, L.A., *Special-Purpose Hardware for Information Retrieval*, in *Information Retrieval, Data Structures and Algorithms*, Prentice-Hall, 1992.
- Hollaar, L.A., *Direct Implementation of Asynchronous Control Units*, IEEE Trans. on Computers, Dec. 1982.

#### **John M. Hollerbach**

Professor of Computer Science

Ph.D., Massachusetts Institute of Technology, 1978

Professor Hollerbach’s<sup>11</sup> interests include robotics, teleoperation, virtual reality, and human motor control. Autonomous and teleoperated grasping is pursued with a pair of left/right Utah/MIT Dextrous Hands. Teleoperation research focuses on the use of the Sarcos Dextrous Arm Master and Slave, an advanced hydraulic telemanipulator. In virtual reality, the focus is on improving the transparency and sense of immersion through better mechanical interfaces and their control, better visual and auditory displays, and sensorimotor integration. More specifically, haptic interfaces are being employed for virtual

<sup>10</sup><http://www.cs.utah.edu/~hollaar/>

<sup>11</sup><http://www.cs.utah.edu/~jmh/>

manipulation of Alpha1 CAD models and for scientific visualization. The Treadport locomotion interface is employed for walk-in synthetic environments. Real-time architectures employing the Myrinet for low-latency networking of devices, computations, and graphics are being developed. Methods for perturbation analysis and nonlinear system identification of joint mechanical properties are being developed, for use in clinical and human operator dynamics settings.

- Christensen, R., Hollerbach, J.M., Xu, Y., and Meek, S., “Inertial force feedback for a locomotion interface,” *Proc. ASME Dynamic Systems and Control Division, DSC-Vol. 64*, 1998, pp. 119-126.
- Durbeck, L.J.K., Macias, N.J., Weinstein, D.M., Johnson, C.R., and Hollerbach, J.M., “SCIRun haptic display for scientific visualization,” *Phantom Users Group Meeting*, Dedham, MA, September 1998.
- Johnston, D., Zhang, P., Hollerbach, J.M., and Jacobsen, S.C., “A full tactile sensing suite for dextrous robot hands and use in contact force control,” *Proc. IEEE Intl. Conf. Robotics and Automation*, Minneapolis, April 22-28, 1996, pp. 3222-3228.
- Nahvi, A., Nelson, D.D., Hollerbach, J.M., and Johnson, D.E., “Haptic manipulation of virtual mechanisms from mechanical CAD designs,” *Proc. IEEE Intl. Conf. Robotics & Automation*, Leuven, Belgium, May 16-21, 1998, pp. 375-380.
- Xu, Y., and Hollerbach, J.M., “A robust ensemble data method for identification of human joint mechanical properties during movement,” *IEEE Trans. Biomedical Engineering*, 46, 1999, pp. 409-419.

#### **Wilson C. Hsieh**

Assistant Professor of Computer Science

Ph.D., Massachusetts Institute of Technology, 1995

Professor Hsieh<sup>12</sup> joined the Department of Computer Science in September 1997. His research interests are in compilers, programming languages, and systems. His primary interest lies in how compiler and programming language technology can be used to build fast, flexible systems. He has been investigating the use of high-level languages and dynamic code generation in building extensible operating systems; the interaction of compilers and architecture; and compiler optimization for commercial workloads.

- Alastair Reid, John McCorquodale, Jason Baker, Wilson Hsieh, and Joseph Zachary, “The Need for Predictable GC.” In *Proceedings of the Second Workshop on Compiler Support for System Software*, May 1999.
- Godmar Back and Wilson Hsieh, “Drawing the Red Line in Java.” In *Proceedings of HotOS VII*, March 1999.
- John Carter, Wilson Hsieh, Leigh Stoller, Mark Swanson, Lixin Zhang, *et al.*, “Impulse: Building a Smarter Memory Controller.” In *Proceedings of the 1999 Symposium on High Performance Computer Architecture*, January 1999.
- Massimiliano Poletto, Wilson C. Hsieh, Dawson R. Engler, and M. Frans Kaashoek, “‘C and tcc: A Language and Compiler for Dynamic Code Generation.” To appear in *ACM Transactions on Programming Languages and Systems*.
- Wilson C. Hsieh, Marc E. Fiuczynski, Przemysław Paradyk, and Brian N. Bershad, “Type-safe Casting.” In *Software: Practice and Experience*. September 1998.

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<sup>12</sup><http://www.cs.utah.edu/~wilson/>

**Christopher R. Johnson**

Associate Professor of Computer Science  
 Research Associate Professor of Physics and Bioengineering  
 Adjunct Associate Professor of Mathematics  
 Ph.D., University of Utah, 1989

Professor Johnson's<sup>13</sup> research interests are in the area of scientific computing. Particular interests include inverse and imaging problems, adaptive methods for partial differential equations, numerical analysis, computational steering and problem solving environments, computational problems in medicine, and scientific visualization. In 1992, Professor Johnson was awarded a Young Investigator's Award from the NIH, in 1994 he was awarded the National Young Investigator (NYI) Award from the NSF, and in 1995 he was awarded the Presidential Faculty Fellow (PFF) Award from the NSF. In 1996 he received a DOE Computational Science Award and in 1997 received the Par Excellence Award from the University of Utah Alumni Association and the Presidential Teaching Scholar Award. This past year, Professor Johnson was Awarded the Governor's Medal for Science and Technology from Utah Governor Michael Leavitt. He directs the Center for Scientific Computing and Imaging and is Co-Director of the Computational Engineering and Science Program.

- C.R. Johnson. Adaptive Finite Element and Local Regularization Methods for the Inverse ECG Problem. In *Inverse Problems in Electrocardiography*, Peter Johnston, Editor, Advances in Computational Medicine, WIT Press, Southampton, UK, 1999 (to appear).
- Y. Livnat, S. Parker, and C.R. Johnson. Fast isosurface extraction methods for large imaging datasets. In *Handbook of Medical Image Processing*, Isaac Bankman, Editor-in-chief, 1999 (to appear).
- M. Miller, C. Hansen, and C.R. Johnson. Simulation steering with SCIRun in a distributed environment. In *Applied Parallel Computing, 4th International Workshop, PARA'98*, B. Kgstrm, J. Dongarra, E. Elmroth, and J. Wasniewski, eds., Lecture Notes in Computer Science, Springer-Verlag, **volume** 1541, pp. 366-376, 1998.
- C.R. Johnson and R.M. MacLeod. Adaptive local regularization methods for the inverse ECG problem. *Progress in Biophysics and Biochemistry*, Vol. 69, pp. 405-423, 1998.
- S.G. Parker, M. Miller, C.D. Hansen, and C.R. Johnson. An integrated problem solving environment: the SCIRun computational steering system. *31st Hawaii International Conference on System Sciences (HICSS-31)*, **vol. VII**, pp. 147-156, IEEE Press, 1998.
- H. Casanova, J. Dongarra, C.R. Johnson, and M. Miller, Application-specific Toolkits, In *Computational Grids*, I. Foster and C. Kesselman, eds., Morgan Kauffman, 1998.

**Robert Kessler**

Professor of Computer Science  
 Ph.D., University of Utah, 1981

Professor Kessler<sup>14</sup> is currently serving as Chairman of the Department of Computer Science. His current research interests are in agents, software engineering, distributed systems, and visual programming. In the early 90's, he founded the Center for Software Science, a state of Utah Center of Excellence. He has also founded several startup companies and is currently involved with an Internet startup company, emWare, as a member of the board. He has served as member-at-large of ACM SIGPLAN and Vice Chairman for Conferences for SIGPLAN. He recently completed a seven year assignment as co-editor-in-chief of the International Journal of Lisp and Symbolic Computation.

- L. Williams and R. Kessler "All I Really Need to Know about Pair Programming I Learned in Kindergarten," accepted with revisions, to appear in Communications of the ACM.
- N. Dykman, M. Griss, and R. Kessler "Nine Suggestions for Extending UML Extensibility," to appear in UML 99 (October 1999).

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<sup>13</sup><http://www.cs.utah.edu/~crj/>

<sup>14</sup><http://www.cs.utah.edu/~kessler/>

- M. Griss, G. Bolcher, Q. Chen, R. Kessler, and L. Osterweil “Agents and Workflow – An Intimate Connection, or Just Friends?” to appear Tools 99 (August 99), Santa Barbara, CA.
- T. Henderson, M. Dekhil, R. Kessler, and M. Griss “Sensor Fusion,” in “Control Problems in Robotics and Automation,” Edited by Bruno Siciliano and Kimon Valavanis, Springer-Verlag, London, Volume in the Series of Lecture Notes in Control and Information Science, Series Editor: Prof. M. Thoma, 1998, pp. 193-207.
- R. Kessler and M. Griss “Building Object-Oriented Instrument Kits,” Object Magazine, April 1996. [Also available as HP Laboratories Technical Report - HPL-96-22.]

### Jay Lepreau

Research Assistant Professor of Computer Science  
B.S., University of Utah, 1983

Professor Lepreau<sup>15</sup> has been leading OS research in the Department of Computer Science since 1992 as the Assistant Director of the Computer Systems Laboratory, joining the faculty in 1997. His research interests focus on operating systems, expanding into many other areas related to building secure, flexible, and high performance systems. These interests include information security, networks, programming and domain-specific languages, compilers, distributed systems, and software assurance and engineering. He heads the Flux research group, leading DARPA-sponsored research to develop a secure, flexible, and high-performance operating system, with user-level but strong management of arbitrary resources, such as memory, the cpu, and the network. Professor Lepreau also leads Utah’s Active Networks effort, attempting to develop a router OS that can safely and speedily “execute” code-carrying packets. In these efforts, his group has developed much software, including the Flick IDL compiler, the OSKit, the Fluke/Flask OS, the “Alta” Java virtual machine, and the OMOS linker and object server. In 1994 he founded the *Usenix/ACM/IEEE Symposium on Operating Systems Design and Implementation* (OSDI) conference series, and served as its first program chair.

- Ray Spencer, Stephen Smalley, Peter Loscocco, Mike Hibler, Dave Andersen, Jay Lepreau. The Flask Security Architecture: System Support for Diverse Security Policies. To appear in *Proceedings of the Eighth Usenix Security Symposium*, August 1999.
- Bryan Ford, Mike Hibler, Roland McGrath, Patrick Tullmann, Jay Lepreau. Interface and Execution Models in the Fluke Kernel. In *Proceedings of the Third Usenix/ACM/IEEE Symposium on Operating Systems Design and Implementation*, February 1999.
- Patrick Tullmann and Jay Lepreau. Nested Java Processes: OS Structure for Mobile Code. Presented at and appears in *Proceedings of the Eighth ACM SIGOPS European Workshop*, September 1998.
- Bryan Ford, Godmar Back, Greg Benson, Jay Lepreau, Albert Lin, Olin Shivers. The Flux OSKit: A Substrate for OS and Language Research. In *Proceedings of the 16th ACM Symposium on Operating Systems Principles*, October 1997.
- Eric Eide, Kevin Frei, Bryan Ford, Jay Lepreau, Gary Lindstrom. Flick: A Flexible, Optimizing IDL Compiler. In *Proceedings of the ACM SIGPLAN 1997 Conference on Programming Language Design and Implementation*, June 1997.
- Bryan Ford, Mike Hibler, Jay Lepreau, Patrick Tullmann, Godmar Back, Stephen Clawson. Microkernels Meet Recursive Virtual Machines. In *Proceedings of the Second Usenix/ACM/IEEE Symposium on Operating Systems Design and Implementation*, October 1996.

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<sup>15</sup><http://www.cs.utah.edu/~lepreau/>

**Gary E. Lindstrom**  
 Professor of Computer Science  
 Ph.D., Carnegie-Mellon University, 1971

Professor Lindstrom's<sup>16</sup> research interests include programming languages, databases, and parallel and distributed computing. He is on the editorial board of *International Journal of Parallel Programming*, and was Editor-in-Chief from its founding until 1993. With Doug DeGroot, he co-edited the book *Logic Programming: Functions, Relations and Equations* published by Prentice-Hall. Professor Lindstrom has been a member of the National Science Foundation Computer and Computation Research Advisory Committee, and served as a Distinguished Visitor of the IEEE Computer Society. In 1981 he received the College of Engineering Outstanding Teaching Award.

- Guruduth Banavar, Gary Lindstrom. Etyma: A Framework for Applying Inheritance beyond Class-Based Languages, Chapter in *Object-Oriented Application Frameworks*, Mohamed Fayad, editor, 1998. John Wiley and Sons, Inc.
- Jon Oler, Gary Lindstrom, and Terence Critchlow. Migrating relational data to an OODB: strategies and lessons from a molecular biology experience. In *Proc. of the Conference on Object-Oriented Programming, Systems, Languages, and Applications*, Atlanta, GA, October 1997. ACM SIGPLAN.
- Eric Eide, Kevin Frei, Bryan Ford, Jay Lepreau, and Gary Lindstrom. Flick: a flexible, optimizing IDL compiler. In *Proc. of the Symposium on Programming Language Design and Implementation*, pages 44–56, Las Vegas, NV, June 1997. ACM SIGPLAN.
- Guruduth Banavar and Gary Lindstrom. An application framework for modular composition tools. In *Proc. of the European Conference on Object-Oriented Programming*, pages 91–113, Linz, Austria, July 1996. Springer LNCS 1098.
- Rob Sargent, Dave Fuhrman, Terence Critchlow, Tony Di Sera, Robert Mecklenburg, Gary Lindstrom and Peter Cartwright. The design and implementation of a database for human genome research. In *Proc. of the Eighth International Conference on Scientific and Statistical Database Management*, pages 220–225, Stockholm, June 1996. IEEE Computer Society Press.
- Guruduth Banavar, Douglas Orr and Gary Lindstrom. Layered, Server-based Support for Object-Oriented Application Development. In *Proc. of the International Workshop on Object-Oriented Operating Systems*, Lund, Sweden, August 1995.

**Sally McKee**  
 Research Assistant Professor of Computer Science  
 Ph.D., University of Virginia, 1995

Professor McKee<sup>17</sup> joined the Department in 1998. Her research interests lie in the intersection of computer architecture, operating systems, compilers, and performance evaluation. Her current work focuses on memory system design and analysis. Dr. McKee is a member of the Impulse Adaptable Memory Systems project, where she is helping to build a computer system that gives applications more control over how their data is accessed. Another project investigates how to give the memory hierarchy knowledge of future access patterns, and then how best to exploit that knowledge to improve system performance. A third project seeks to develop an analytical method for designing and evaluating better complex cache hierarchies.

- L. Zhang, J.B. Carter, W.C. Hsieh, and S.A. McKee. Memory System Support for Image Processing. In the *Proceedings of the 1999 International Conference on Parallel Architectures and Compilation Techniques*, October 1999.
- S.I. Hong, S.A. McKee, M.H. Salinas, R.H. Klenke, J.H. Aylor, and Wm.A. Wulf. Access Order and Effective Bandwidth for Streams on a Direct Rambus Memory. In the *Proceedings of the Fifth International Symposium on High Performance Computer Architecture*, pp. 80-89, January 1999.

<sup>16</sup><http://www.cs.utah.edu/~gary/>

<sup>17</sup><http://www.cs.utah.edu/~sam/>

- S.A. McKee, et al. Smarter Memory: Improving Bandwidth for Streamed References. IEEE Computer, pp. 54-63, July 1998.
- D.A.B. Weikle, S.A. McKee, and Wm.A. Wulf. Caches As Filters: A New Approach to Cache Analysis. In the *Proceedings of the Sixth International Symposium on Modeling, Analysis, and Simulation of Computer and Telecommunication Systems*, July 1998.
- Wm.A. Wulf and S.A. McKee. Hitting the Memory Wall: Implications of the Obvious. Computer Architecture News, 23(1):20-24, March 1995.

**Richard F. Riesenfeld**

Professor of Computer Science  
Ph.D., Syracuse University, 1973

Professor Riesenfeld<sup>18</sup> has been involved in research in the areas of computer graphics, animation, computer aided geometric design and CAD/CAM since joining the Computer Science faculty in 1972. Recently he has been investigating a broad spectrum of research problems in computer graphics, geometric modeling, and manufacturing within an integrated experimental testbed system motivated by the unifying principles of spline theory.

- R.F. Riesenfeld, R. Fish, and S. Drake, "A Case Study in Multi-disciplinary Distributed Collaborative Design," in Proc. ASME Conference on Network-Centric CAD, in press, Sept. 1997.
- M. Sturgill, E. Cohen, and R. Riesenfeld, "Feature Based 3-D Sketching for Early Design," Proceedings of the 1995 ASME Computers in Engineering Conference
- R. Riesenfeld, "Some Video Related High Bandwidth Communications Needs," in Proc. NSF Workshop on vBNS and the Research Agenda for Networking and Applications, June 1995.  
*Fundamental Developments of Computer Aided Geometric Design*, L. Piegl (ed.), Academic Press, 1993.
- Y.C. Hsieh, R. F. Riesenfeld, and S. H. Drake, "Reconstruction of Sculptured Surfaces using Coordinate Measuring Machine", ASME Design Automation Conference, 1993.

**Ellen M. Riloff**

Assistant Professor of Computer Science  
Ph.D., University of Massachusetts at Amherst, 1994

Professor Riloff<sup>19</sup> joined the department in 1994. Her research interests are in natural language processing, machine learning, and artificial intelligence. She is particularly interested in techniques for automatically generating dictionaries and knowledge bases for natural language processing. Most of her work revolves around the task of *information extraction*, which involves extracting information from text. The NLP research group at Utah has built its own NLP system called Sundance, which is a partial parser that activates and instantiates case frames for information extraction. Current research projects include bootstrapping techniques for learning extraction patterns, corpus-based techniques for learning semantic dictionaries, and corpus-based methods for coreference resolution.

- Riloff, E. and Jones, R., "Learning Dictionaries for Information Extraction by Multi-Level Bootstrapping," in *Proceedings of the Sixteenth National Conference on Artificial Intelligence (AAAI-99)*. 1999.
- Bean, D. and Riloff, E., "Corpus-Based Identification of Non-Referential Noun Phrases," in *Proceedings of the 37th Annual Meeting of the Association for Computational Linguistics (ACL-99)*. 1999.
- Riloff, E. "Information Extraction as a Stepping Stone toward Story Understanding," in *Computational Models of Reading and Understanding*. The MIT Press. 1999.

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<sup>18</sup><http://www.cs.utah.edu/~rfr/>

<sup>19</sup><http://www.cs.utah.edu/~riloff/>

- Riloff, E. and Shepherd, J., "A Corpus-Based Approach for Building Semantic Lexicons," in *Proceedings of the Second Conference on Empirical Methods in Natural Language Processing*. 1997.
- Riloff, E., "Automatically Generating Extraction Patterns from Untagged Text," in *Proceedings of the Thirteenth National Conference on Artificial Intelligence (AAAI-96)*. 1996.

### Peter Shirley

Associate Professor of Computer Science  
Ph.D., University of Illinois, 1991

Professor Shirley<sup>20</sup> joined the Department in 1996. He is interested in creating highly realistic images of virtual environments, and visualization of complex data. The former involves explicit and procedural generation of geometric models with realistic optical characteristics, light transport simulation to determine the outgoing light distribution from surfaces, and tone reproduction to create images displayable on low dynamic range media such as paper and CRTs. The latter involves issues of visual representation of complex data, as well as strategies for navigation and interaction that help the user extract local and global information about the data. In 1998 he received the College of Engineering Outstanding Teacher Award.

- A. J. Preetham, P. Shirley, and B. Smits. A Practical Analytic Model for Daylight. In *ACM SIGGRAPH Annual Conference*, 1999.
- S. Premoze, W. Thompson, and P. Shirley. Geospecific Rendering of Alpine Terrain. In *Eurographics Rendering Workshop*, 1999.
- B. Gooch, P. Sloan, A. Gooch, P. Shirley and R. Riesenfeld. Interactive Technical Illustration. In *Symposium on Interactive 3D Computer Graphics*, 1999.
- P. Shirley and K. Chiu. A Low Distortion Map Between Disk and Square. In *Journal of Graphics Tools*, 1998.
- A. Gooch, B. Gooch, P. Shirley, and E. Cohen. A Non-photorealistic Lighting Model for Automatic Technical Illustration. In *ACM SIGGRAPH Annual Conference*, 1998.

### Kris Sikorski

Associate Professor of Computer Science  
Ph.D., University of Utah, 1982

Professor Sikorski's<sup>21</sup> current research interests are in the areas of distributed parallel scientific computation and computational complexity with emphasis on information based complexity. Of specific interest are applied problems in geophysics (3-D modeling of earthquakes), combustion (fluid mechanics), and electromagnetic wave propagation (Maxwell equations). Various parallel explicit and implicit algorithms are being studied and implemented on massively parallel machines. Information based complexity is a study of optimal algorithms for problems which are approximately solved, because of partial and contaminated information. Optimal algorithms for solving nonlinear problems with use of various error criteria are of special interest to Professor Sikorski.

- Sikorski, K., Tsay, C., and Wozniakowski, H., *An Ellipsoid Algorithm for the Computation of Fixed Points*, Journal of Complexity, March, 1993.
- Sikorski, K., Schuster, J., and Tsay, C., *3-Dimensional Modeling of Acoustic and Elastic Wave Propagation on Parallel Computers*, Transputer Research and Applications 1, IOS Press, 1990.
- Sikorski, K., and Wozniakowski, H., *Complexity of Fixed Points I*, Journal of Complexity, December, 1987.
- Boulton, T., and Sikorski, K., *Complexity of Computing Topological Degree of Lipschitz Functions in N-Dimensions*, Journal of Complexity, Vol. 2, pp. 44-59, 1986.

<sup>20</sup><http://www.cs.utah.edu/~shirley/>

<sup>21</sup><http://www.cs.utah.edu/~sikorski/>

- Sikorski, K., and Wozniakowski, H., *For Which Error Criteria Can We Solve Nonlinear Equations*, Journal of Complexity, 1986.

### **Kent F. Smith**

Professor of Computer Science  
 Professor of Electrical Engineering  
 Ph.D., University of Utah, 1982

Professor Smith's<sup>22</sup> principal interests lie in the design of integrated circuits using computer aids for structured logic. His present research is focused on techniques for high speed GaAs integrated circuits. This research involves the detailed design and fabrication of the actual integrated circuits as well as Computer Aided Design (CAD) tools for the design of these circuits. The GaAs circuits he is working with operate at speed in excess of 10 GHz and thus present problems involving very difficult analysis. For example, high speed design requires that the components be physically placed near each other to give optimum performance. In addition, the actual interconnecting wires between logic gates must be characterized and used in the analysis. The extraction and use of these high speed parameters represents problems that require new methods of design. Prior to joining the University of Utah faculty, Professor Smith was responsible for integrated circuit design and testing at the Microcircuit Laboratory at the University of Utah Research Institute. Prior to that time he was the technical director for Electrical Engineering at the General Instrument Advanced Microelectronics Lab. He holds a number of patents in circuits primarily concerning MOS and bipolar integrated circuits.

- Carter, Tony and Smith, K. F., et. al. *Cell Matrix Methodologies for Integrated Circuit Design* Integration, The VLSI Journal, July, 1989.
- Smith, K.F. and Gu, Jun, *A structured approach for VLSI Circuit Design*, IEEE Computer, Vol 22, No. 11, November, 1989, pages 9-22.

### **Brian Smits**

Research Assistant Professor of Computer Science  
 Ph.D., Cornell, 1994

Professor Smits<sup>23</sup> has been working in the areas of global illumination, ray tracing, and terrain rendering. His work in global illumination has been directed towards making radiosity effective in complex environments through a process of bounding errors in the computations. Recently he has been investigating the synthesis of Monte Carlo techniques with finite element methods for global illumination. In the area of ray tracing, he has done some work with interactive ray tracing on large, shared memory, systems. He has also looked at lower-level efficiency issues for reducing the cost of ray tracing. He is currently looking at automatic generation of improved acceleration hierarchies for ray tracing. His work in terrain rendering has been directed towards improving the models of atmospheric effects and day light for faster, more accurate, renderings of outdoor environments.

- Smits, B., "Efficiency Issues for Ray Tracing," Journal of Graphics Tools (In Press), 1999.
- Preetham, A. J., Shirley, P., Smits, B., "A Practical Analytic Model for Daylight", In *SIGGRAPH 1999*.
- Parker, S., Martin, W., Sloan, P.-P., Shirley, P., Smits, B., and Hansen, C., "Interactive Ray Tracing," In Proceedings of *Symposium for Interactive 3D Graphics*
- B. Smits, J. Arvo, D. Greenberg, "A Clustering Algorithm for Radiosity in Complex Environments," In *SIGGRAPH 1994*.
- B. Smits, J. Arvo, D. Salesin, "An Importance-Driven Radiosity Algorithm," In *SIGGRAPH 1992*.

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<sup>22</sup><http://www.cs.utah.edu/~k-smith/>

<sup>23</sup><http://www.cs.utah.edu/~bes/>

**Frank Stenger**  
 Professor of Computer Science  
 Ph.D., University of Alberta, 1965

Professor Stenger's<sup>24</sup> research interests include the development of new methods of computation and the computer solution of computationally difficult problems from science and engineering, such as inverse problems, crack problems, flow problems and heat problems. He developed the Sinc methods, which provide optimal algorithms in all areas of engineering computation. He is currently writing, jointly with Michael O'Reilly and Tao Zhang, a *Sinc Tool Box* computer-based tutorial package to make these methods accessible to users. One of his students (Kenneth Parker) has recently completed his Ph.D. dissertation *PTOLEMY: A Sinc-Collocation Mapping Sub-System*, which is a computer sub-package of *Maple* that automates the solution of partial differential equations. Several of his students have recently written or are presently writing computer packages for solving a broad range of difficult engineering problem—such as Navier-Stokes equations (Barkey and Vakili, Narasimhan), Maxwell equations (Naghsh-Nilchi) based on his recently discovered Sinc method of approximating indefinite convolutions, which leads to a unified approach for solving elliptic, parabolic, and hyperbolic differential equations. Another student (Ross Schmittlein) is writing a package based on Sinc, constructing conformal maps. Stenger and his former student O'Reilly have been developing methods which make it possible to invert the Helmholtz equation without computing the forward solution. During the next few academic years he expects to extend these inversion methods so that they can be applied to rendering, to visualization, to the determination of paths for robots, and to the inversion of heat and electromagnetic problems for medical and geophysical applications. Seven of his papers have been accepted for publications during this past academic year.

- R. Kress, I.H. Sloan, and F. Stenger, *A Sinc Quadrature Method for the Double-Layer Integral Equation in Planar Domains With Corners*, with R. Kress and I.H. Sloan, to appear in volume honoring P.M. Anselone's 65<sup>th</sup> birthday.
- F. Stenger, B. Keys, M. O'Reilly, and K. Parker, *ODE-IVP—PACK, via Sinc Indefinite Integration and Newton Iteration*, to appear in "Numerical Algorithms".
- F. Stenger, *A Unified Approach to the Approximate Solution of PDE*, to appear in "Communications in Applied Analysis", 1998.
- F. Stenger, R. Kress, and I.H. Sloan, *Constructing Conformal Maps via Sinc Methods*, prepared for the Cyprus Proceedings on Computational Complex Variables, Oct., 1997.
- A. Naghsh-Nilchi and F. Stenger, *Solution of the Electric Field Integral Equations via Sinc Convolution*, submitted.

**William B. Thompson**  
 Professor of Computer Science  
 Ph.D., University of Southern California, 1975

Professor Thompson's<sup>25</sup> primary research interest is in the area of visual perception. Currently, he is exploring how an understanding of computational vision can aid in improving the spatial information conveyed by graphical displays. He is also active in the exploration of techniques for analyzing visual motion, sensing for manufacturing, and vision-based navigation. Professor Thompson joined the University of Utah in 1991 after 16 years in the Computer Science Department at the University of Minnesota.

- S. Premoze, W.B. Thompson, and P. Shirley, "Geospecific Rendering of Alpine Terrain," *Proceedings of the Eurographics Rendering Workshop*, June 1999.  
 W.B. Thompson, J.C. Owen, H.J. de St. Germain, Stevan R. Stark, and T.C. Henderson, "Feature-Based Reverse Engineering of Mechanical Parts," *IEEE Transactions on Robotics and Automation*, 15(1), February 1999.
- W.B. Thompson, "Exploiting Discontinuities in Optical Flow," *International Journal of Computer Vision*, 30(3), 1998.  
 H.L. Pick, Jr., M.R. Heinrichs, D.R. Montello, K. Smith, C.N. Sullivan, and W.B. Thompson, "Topographic Map Reading," in *Ecology of Human-Machine Systems*, J. Flach, P. Hancock, J. Caird, and K. Vicente, eds., 1995.

<sup>24</sup><http://www.cs.utah.edu/~stenger/>

<sup>25</sup><http://www.cs.utah.edu/~thompson/>

- K.T. Sutherland and W.B. Thompson. Localizing in unstructured environments: Dealing with the errors. *IEEE Transactions on Robotics and Automation*, December 1994.

**Joseph L. Zachary**

Clinical Professor of Computer Science  
Ph.D., Massachusetts Institute of Technology, 1987

Professor Zachary<sup>26</sup> joined the Department in 1987. He is interested in finding ways to use computers in teaching science and engineering in general, and computer science in particular. He is currently developing Web-based tools and curricula for teaching introductory programming and computer science courses. Professor Zachary received the University of Utah Distinguished Teaching Award in 1997, was named a Department of Energy Undergraduate Computational Science Education Award winner in 1996, was a University of Utah Presidential Teaching Scholar in 1995, and received the College of Engineering Outstanding Teaching Award in 1990.

- J.L. Zachary. *Introduction to Scientific Programming: Computational Problem Solving Using Mathematica and C*. TELOS/Springer-Verlag, 1998.
- J.L. Zachary. *Introduction to Scientific Programming: Computational Problem Solving Using Maple and C*. TELOS/Springer-Verlag, 1996.
- J.L. Zachary. The gestalt of scientific programming: Problem, model, method, implementation, assessment. In *Papers of the Twenty-Eighth SIGCSE Technical Symposium*. ACM Press, Feb 1997.
- A.H. Lee and J.L. Zachary. Reflections on metaprogramming. *Transactions on Software Engineering*, November 1995.
- J.L. Zachary. Tutorial-based teaching of introductory programming classes. In *Papers of the Twenty-Fifth SIGCSE Technical Symposium on Computer Science Education*. ACM Press, March 1994.

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<sup>26</sup><http://www.cs.utah.edu/~zachary/>



# 6

## Current Funded Research

As an indication of the breadth and specialties of current research activity in the department, the following list summarizes a partial list of funded projects underway at the time of this handbook's publication.

- *Application Driven Advancement of Asynchronous Design Methods*, NSF, Erik Brunvand and Ganesh Gopalakrishnan.
- *Center of Excellence for Asynchronous Circuit and System Design*, State of Utah, Erik Brunvand and Chris Myers.
- *Adaptive Structure Aware Memory Systems*, DARPA, John Carter.
- *Rapid Extraction of Isosurfaces from Time-Varying Data*, LLNL, Charles Hansen.
- *Undergraduate Experience in Research*, SNL, Charles Hansen.
- *Graduate Research Traineeships*, NSF, Thomas Henderson.
- *Center for the Simulation of Accidental Fires and Explosions C-Safe*, DOE, Al Davis, Charles Hansen, Thomas Henderson, Chris Johnson, Gary Lindstrom.
- *vBNS Connection to the Internet*, NSF, Thomas Henderson.
- *Digital Signatures for Clinical Laboratory Documentation Management*, University of Utah Research Foundation, Lee Hollaar.
- *Rapid Virtual Prototyping of Mechanical Assemblies*, NSF, John Hollerbach.
- *CAREER: Compiler Optimizations for Instruction Bandwidth*, NSF, Wilson Hsieh.
- *Presidential Faculty Fellowship*, NSF, Chris Johnson.
- *Center of Excellence "Center for Scientific Computing and Imaging"*, State of Utah, Chris Johnson.
- *NSF Program for Partnerships for Advanced Computational Infrastructure*, NSF, Chris Johnson.
- *ASCI Advanced Visualization Technology Center*, DOE, Chris Johnson and Charles Hansen.
- *JANOS: A Java-oriented Active Network Operating System*, DARPA, Jay Lepreau.

- *A Large-Scale Network Testbed*, DEC/Compaq, Novell, University of Utah; Jay Lepreau.
- *Understanding and Improving Memory System Performance*, NSF, Sally A. McKee.
- *Multiphase Integrated Engineering Design (MIND)*, DARPA, Richard Riesenfeld and Elaine Cohen.
- *Computer Graphics and Scientific Visualization*, NSF Science and Technology Center, Richard Riesenfeld and Elaine Cohen.
- *Acquisition of Equipment for Telecollaboration, Telepresence, and Design*, NSF, Rich Riesenfeld.
- *CAREER: Building Conceptual Natural Language Processing Systems for Practical Applications*, NSF, Ellen Riloff.
- *Information Extraction as a Basis for Multi-Faceted Text Categorization Systems*, NSF, Ellen Riloff.
- *Realistic Computer Graphics for Natural Scenes*, NSF, Peter Shirley, Brian Smits, William Thompson.
- *User-Directed Hybrid Deterministic and Monte Carlo Parallel Light Transport Algorithms*, NSF, Peter Shirley, Charles Hansen.
- *Rapid Prototyping of Analog/Digital Circuits*, FBI, Kent Smith.
- *CISE Research Infrastructure: Modeling Complex Physical and Computational Environments*, NSF, William Thompson.





15. Undergraduate major(s): \_\_\_\_\_

16. List in chronological order **all** colleges and universities you have attended, including the University of Utah, regardless of length of attendance. Two official copies of transcripts from **each** school you have attended must be sent **directly** from the institution to the Department of Computer Science. Transcripts must be received before any action can be taken.

*For international students:* School records must be submitted in the original language with official English translations. Please list the diploma, certificate, or degree received in the original terminology rather than attempting to translate into American terminology. For example, if you received a Diploma, write Diploma, rather than bachelor's degree.

**NOTE: Failure to list ALL institutions previously attended or degrees pursued may result in loss of credit and dismissal from the U of U. Also, if you were previously enrolled in a graduate program which was not completed, please provide a written statement explaining the reasons for noncompletion.**

Name of Institution	Location	Dates of Attendance		Degree Earned & Date Received/Expected
		From (month/year)	To (month/year)	
_____	_____	_____	_____	_____
_____	_____	_____	_____	_____
_____	_____	_____	_____	_____
_____	_____	_____	_____	_____

17. Indicate what you have been doing (e.g., employment, school, military) and where you have been for the past three years. Be specific and account for all months.

Dates of Activity From (month/year) To (month/year)	Employer or Activity	Location (city, state, country)
_____	_____	_____
_____	_____	_____
_____	_____	_____
_____	_____	_____

18. How did you learn about the department of computer science at the University of Utah?

19. What made you decide to apply to the department of computer science at the University of Utah?

20. Person to notify in case of emergency:

Name: \_\_\_\_\_

Relationship (optional): \_\_\_\_\_ Telephone ( ) \_\_\_\_\_

Address: \_\_\_\_\_

number and street                      city                      state                      zip code                      country

The University of Utah is fully committed to policies of nondiscrimination and equal opportunity, and vigorously pursues affirmative action in all programs, activities, and employment with regard to race, color, national origin, sex, age, and status as a handicapped individual. Religion, sexual orientation, and status as a disabled veteran or veteran of the Vietnam era also are protected under nondiscrimination and equal opportunity employment policies. Evidence of practices not consistent with this policy should be reported to the Office of the President of the University, 801-581-8365. *Any false or misleading information provided in the support of this application may result in dismissal from the Graduate School.*

All the answers I have given in this application are complete and accurate to the best of my knowledge. Form must be completed and signed by applicant.

**Signature:** \_\_\_\_\_ **Date:** \_\_\_\_\_

**Department of Computer Science, University of Utah  
Academic and Professional Reference**

Instructions: This form should be completed by the applicant and the person acting as a reference for the applicant (endorser). The completed form should be sent by the endorser to the address below. The reference must be received by January 15, 2000.

Graduate Coordinator  
University of Utah  
Department of Computer Science  
50 S. Central Campus Drive, RM 3190  
Salt Lake City, UT 84112-9205

---

**Part I: Applicant Information (to be completed by applicant)**

Name: \_\_\_\_\_

Social security number (optional): \_\_\_\_\_

Degree sought:  M.S.  Ph.D.

I waive  / do not waive  my right to have access to this completed *Academic and Professional Reference* form and any attachments.

Signature: \_\_\_\_\_ Date: \_\_\_\_\_

---

**Part II: Endorser Information (to be completed by endorser)**

Name: \_\_\_\_\_

Title: \_\_\_\_\_

Institution: \_\_\_\_\_

Address: \_\_\_\_\_

Telephone: \_\_\_\_\_

E-mail: \_\_\_\_\_

Signature: \_\_\_\_\_ Date: \_\_\_\_\_

---

**Part III: Evaluation of Applicant (to be completed by endorser)**

**Note to the endorser:** The person named above is applying for graduate admission to the department of computer science at the University of Utah, and has listed you as a reference. Please complete this form and *send it directly to the department of computer science at the address specified above*. No action will be taken on the applicant's request until this form is received.

1. Indicate your confidence in your ability to judge the applicant's academic abilities well enough to act as a reference. If you do not know the applicant well, feel free to say so; your frankness will not prejudice the student's application.

- Very confident  
 Confident  
 Somewhat confident  
 Do not know the applicant well

2. In what capacity have you known the applicant? (check all that apply)

- As a student in classes that you taught  
 As a student in an independent study project that you supervised  
 As a participant in a research project that you supervised  
 Other (please specify): \_\_\_\_\_

3. Compared to other students at the same level in your institution, how do you rate the applicant?

Check one:

- The best
- The best in the last 5 years
- The best this year
- Outstanding
- Very good
- Good
- Not recommended for graduate study

4. Do you have any reason to believe that the applicant's scholastic record, as you know it, is not an accurate index of his or her scholastic ability? If so, please explain.

5. Evaluation Profile: please check the appropriate boxes.

	Outstanding	Unusual	Good	Average	Below Average	Unknown
Communication skills (oral & written)						
Motivation						
Analytical thinking						
Independence						
Creativity						
Ability to work with others						

6. I would be pleased to have the applicant working under my direction as a:  
 Research Assistant  Teaching Assistant  Both  Neither

7. Expanding on the Evaluation Profile above, what is your estimation of the applicant's potential as a graduate student?  
 (Attach a separate sheet if necessary.)

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Telephone: \_\_\_\_\_

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Signature: \_\_\_\_\_ Date: \_\_\_\_\_

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